Plans for Integrated Cooling of the AHCAL

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NEED FOR INTEGRATED COOLING

ILC environment:

 power consumption managed through power pulsing (1 millisecond bunch trains at 5 Hz)

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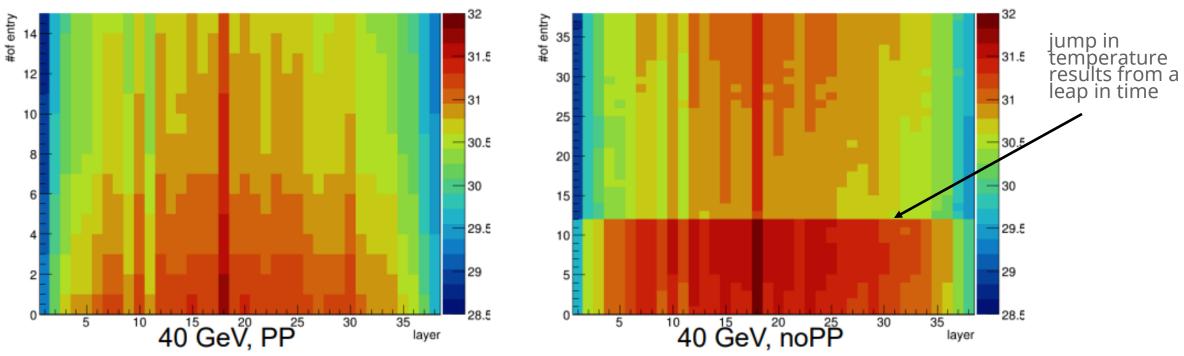
FCC-ee requirement:

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- higher interaction rate
- electronics need to remain continuously powered
- -> Need for integrated cooling

EFFECTS OF POWER PULSING

Cooling with power pulsing, heating without power pulsing



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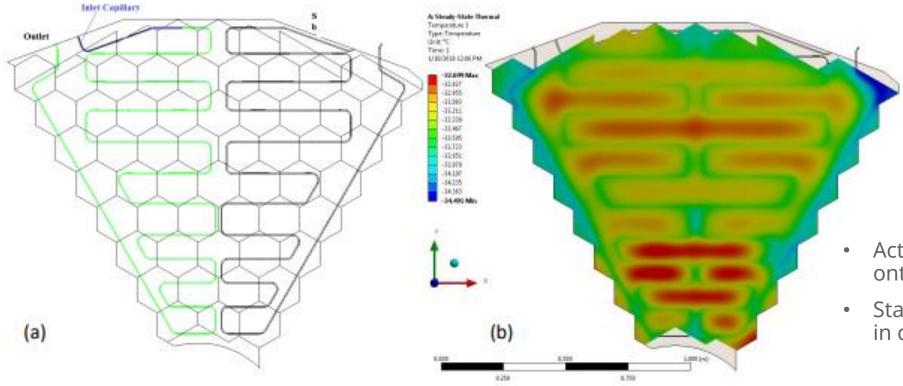
Temperature evolution

Plots taken from Anna Rosmanitz (2018)

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Temperature evolution

CMS HGCAL COOLING



- Active elements mounted directly onto copper plates
- Stainless steal tubes embedded in copper plates

Plot taken from: The Phase-2 Upgrade of the CMS Endcap Calorimeter - CERN Document Server

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FIRST PLANS

Build a dummy HBU using resistors to mimic the power consumption

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Approach 1 (together with the Heidelberg group):

- Use power consumption information from the electronics
- ASICs: 3.34mW/Channel -> 120mW/chip
- Power Boards: $P_{powerboard} = (4V 1.8) * I_{HBUs}$
- -> Simulation needed

FIRST PLANS

Build a dummy HBU using resistors to mimic the power consumption

Andre Klotzbücher

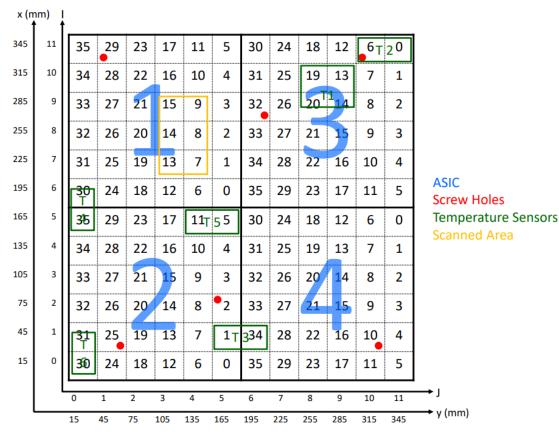
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Approach 2:

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- Using test beam data
- Temperature distributions on the HBUs
- Using gain of SiPMs as temperature measurement

TEMPERATURE DISTRIBUTION ON THE HBU'S



 6 temperature sensors distributed across each HBU

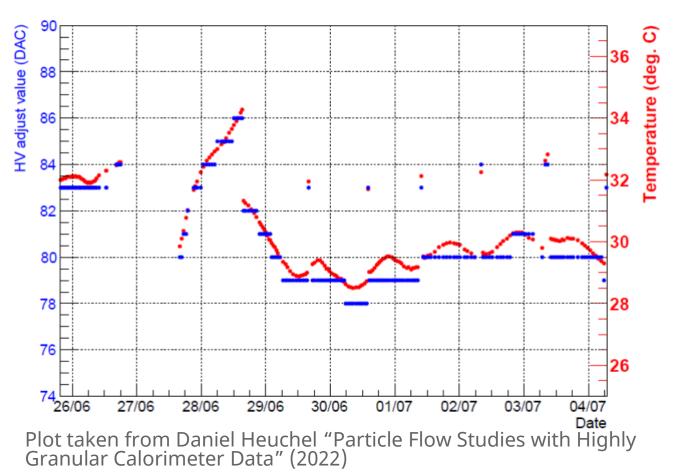
Plot taken from Anna Rosmanitz

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[•] Direct temperature measurement

TEMPERATURE DISTRIBUTION ON THE HBU'S



- Indirect temperature
 measurement
- Using gain of each SiPM as temperature measurement

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REQUIREMENTS FOR THE AHCAL

What should the operating temperature be?

How much space do we have? Does the absorber need to be cooling?

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Can we use plain copper plates cooled on one side, or do we need tubes inside the copper plates?

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