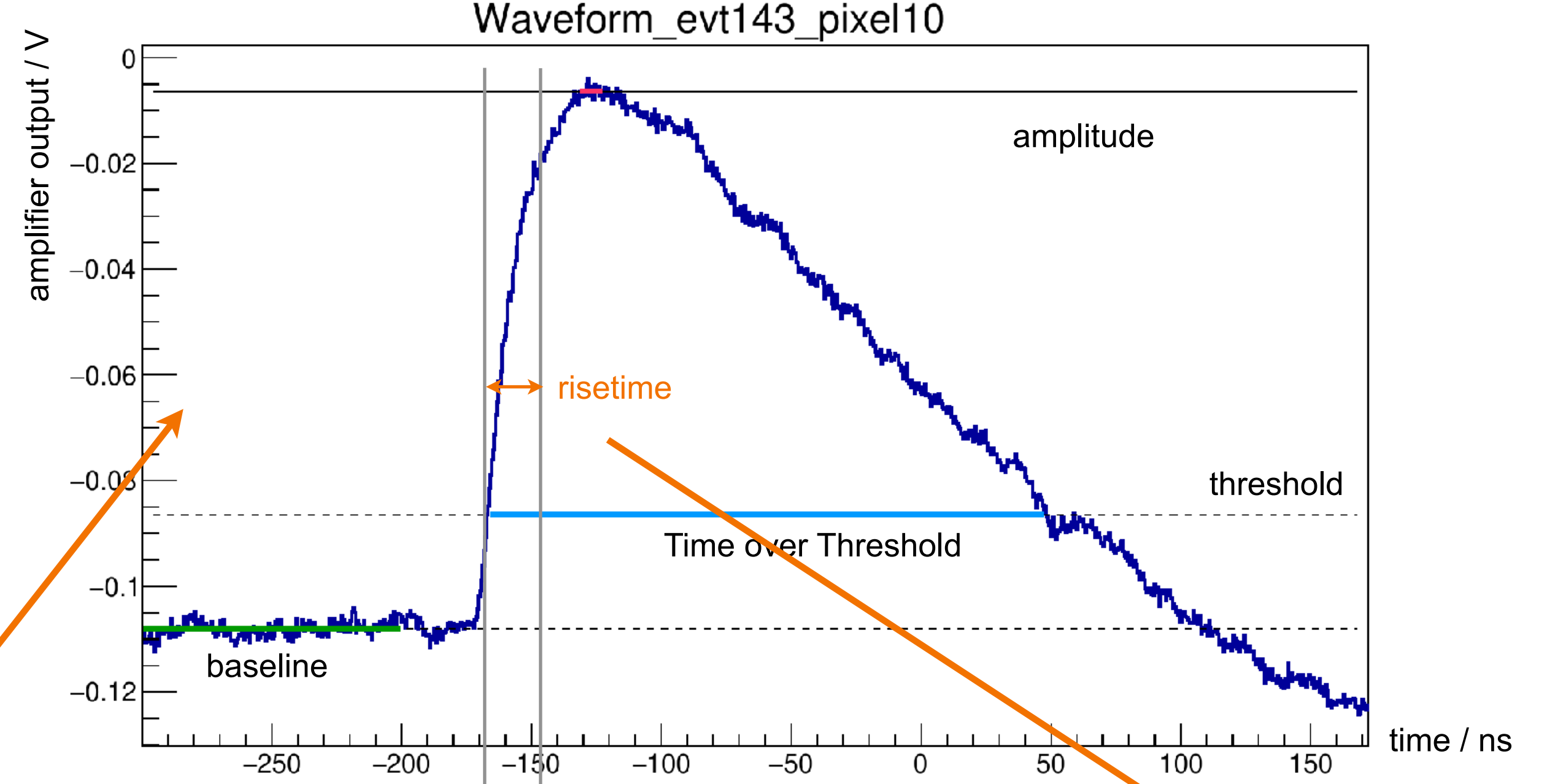


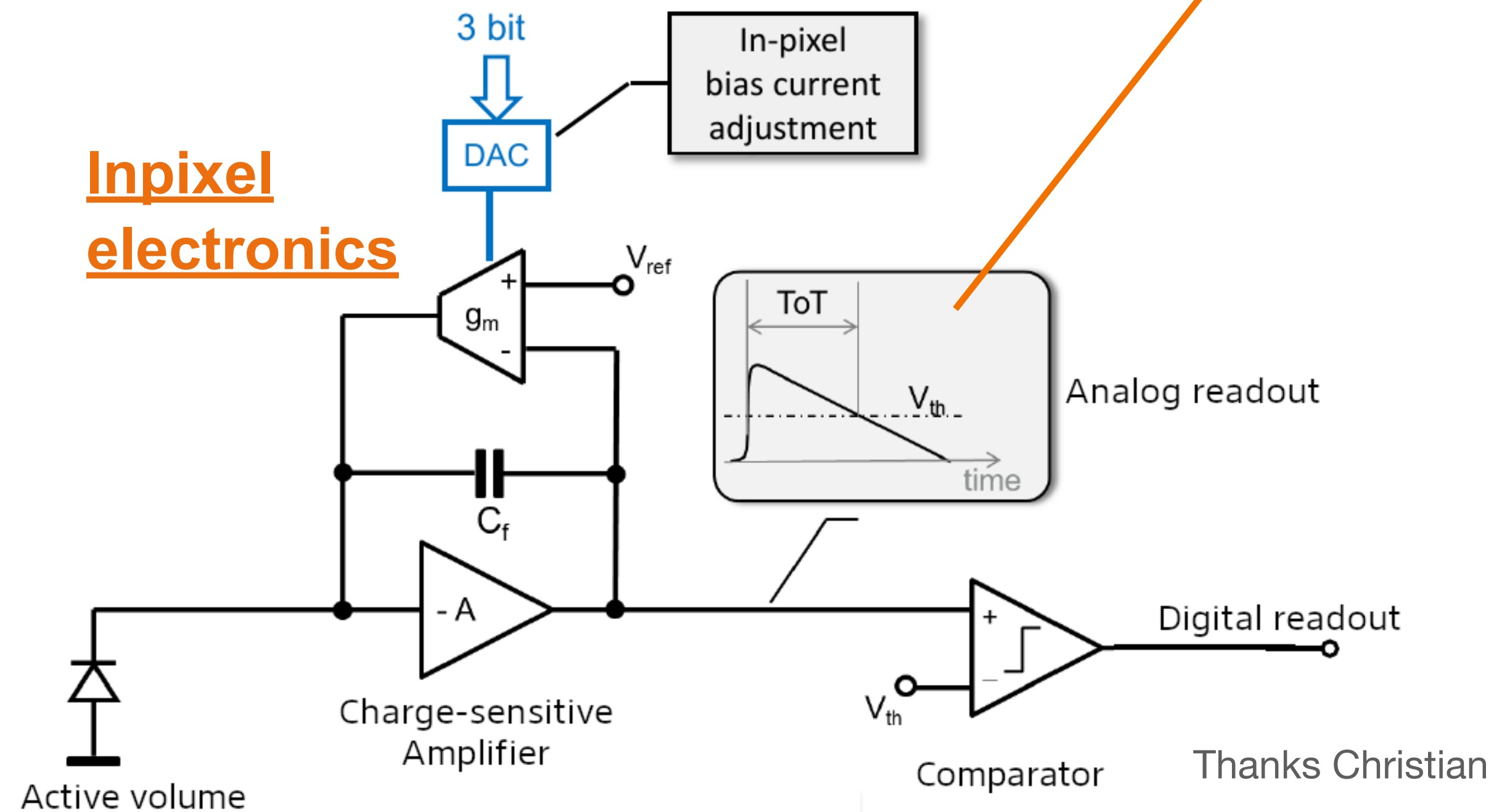
DESY ER1 testchip

65nm CMOS MAPS: pulse shape analysis

- 2x2 pixel matrix, 35 μ m x 25 μ m pitch
- Designed by DESY (Christian!)
- goal: understand whats going on in pixel (hard to understand with fully digital readout, ie. H2M)
- Currently analysing testbeam data (Larissa and me)



Inpixel electronics



DESYER1 - Risetime (median $\pm$ bootstrap, N=5000)

