

Simulations of Hexagonal Pixels

in 65 nm MAPS CMOS Imaging Technology

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1st General Meeting of Lepton Collider Vertex Detector Project
29 November 2024

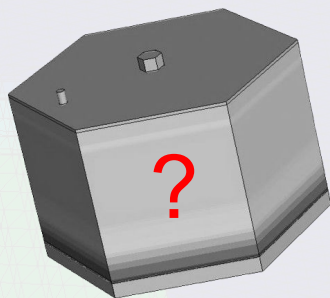
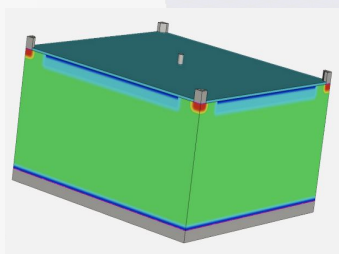


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by Larissa Mendes

Execute **highly detailed** simulations for studying the possibility of using **hexagonal pixels** within a 65 nm CMOS imaging technology for MAPS, with small collection electrodes.



what improvements
can we make by
changing the pixel
shape?

IN-PIXEL EFFICIENCY

Efficiency of **hexagon**
is **smaller** then **square**
pixel for standard.

