

### DPG SPRING CONFERENCE 2025 ADVANCES IN SILICON DETECTORS

Matthias Hamer, University of Bonn





- Introduction
- State of the Art
- Future Detectors
  - Low Mass Detectors
  - Silicon Timing Detectors
  - Calorimetry with Silicon Sensors
  - Further R&D and Other Applications



### • Introduction

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### INTRODUCTION

- requirements for tracking detectors:
  - excellent spatial resolution
  - good signal to noise ratio
  - low  $X/X_0$
  - ...
- silicon detectors can meet all of the requirements
  - micro-structuring
  - no excessive cooling required
  - thin and radiation hard sensors and FE electronics







### INTRODUCTION

- silicon detectors in HEP 2 examples
  - NA11/NA32 lifetime of D-mesons

Abstract. We have measured the lifetime of charged and neutral *D* mesons using high resolution silicon strip detectors in the NA11 spectrometer at the CERN SPS. The *D* mesons were produced in 200 GeV  $\pi^-$ -Be interactions. We obtain a value of  $\tau_{D^{\pm}} = (10.6^{+3.6}_{-2.4}) \cdot 10^{-13}$  s based on 28, and  $\tau_{D^0}$  $= (3.7^{+1.0}_{-0.7}) \cdot 10^{-13}$  s based on 26 fully reconstructed decays for the lifetime of the charged and the neutral *D* meson, respectively. For the ratio  $\tau_{D^{\pm}}/\tau_{D^0}$ we find 2.8<sup>+1.1</sup> -0.8.

![](_page_4_Figure_5.jpeg)

![](_page_5_Picture_0.jpeg)

- silicon detectors in HEP 2 examples
  - NA11/NA32 lifetime of D-mesons
  - Tevatron experiments discovery of the top quark

![](_page_5_Figure_4.jpeg)

![](_page_5_Picture_5.jpeg)

![](_page_6_Picture_0.jpeg)

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![](_page_7_Picture_0.jpeg)

- tracking at the HL-LHC ever increasing requirements
  - dense environments in dense events
  - high hit rates and high data rates
  - high overall power consumption
  - low material budget

![](_page_7_Picture_6.jpeg)

https://cds.cern.ch/record/2674770

![](_page_7_Figure_8.jpeg)

![](_page_8_Figure_0.jpeg)

![](_page_9_Picture_0.jpeg)

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![](_page_10_Picture_0.jpeg)

Particle Physics: Collider Roadmap

![](_page_10_Figure_2.jpeg)

- e<sup>+</sup>e<sup>-</sup> collider for precision physics (Z, H)
- hadron-hadron collider at a later point
- many interesting "smaller accelerator" and "non-accelerator based" experiments along the way

![](_page_11_Picture_0.jpeg)

impact parameter resolution

$$\sigma_{d_0} = a \oplus b/(p \cdot \sin^{\frac{3}{2}} \theta)$$

![](_page_11_Figure_3.jpeg)

L. Gouskos, FCC Week 2024

![](_page_11_Figure_5.jpeg)

### **CLIC Like Detector at the FCC-ee**

- key:
  - good single point resolution (a)
  - low material budget (b)

![](_page_12_Picture_0.jpeg)

in time and space!

#### adding timing to track reconstruction - 4D tracking •

![](_page_12_Figure_2.jpeg)

Advances in Silicon Detectors - Matthias Hamer - DPG 2025

![](_page_13_Picture_0.jpeg)

 $\sigma_t = 10 \text{ ps} \rightarrow \sigma_x = 3 \text{ mm}$ 

vertices are distributed

in time and space!

![](_page_13_Picture_1.jpeg)

### adding timing to track reconstruction - 4D tracking

![](_page_13_Figure_3.jpeg)

10.1088/1361-6633/aa94d3

![](_page_14_Figure_0.jpeg)

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### CALORIMETRY FOR FCC-EE

![](_page_15_Picture_0.jpeg)

- development of detectors for future accelerators
  - roadmap presented for 9 task forces
  - development aligned with collider timeline

![](_page_15_Picture_4.jpeg)

![](_page_15_Figure_5.jpeg)

CERN-ESU-017

![](_page_16_Picture_0.jpeg)

- development of detectors for future accelerators
  - roadmap presented for 9 task forces
  - development aligned with collider timeline

![](_page_16_Picture_4.jpeg)

![](_page_16_Figure_5.jpeg)

CERN-ESU-017

![](_page_17_Figure_0.jpeg)

![](_page_18_Picture_0.jpeg)

### SILICON DETECTOR R&D IN GERMANY

Si-D Consortium							
WP1: Position-Sensitive Monolithic Detectors Dingfelder, Weber		WP2: Fast Timing Garutti, Galatyuk					
WP 1.1 CMOS tracking detectors Bonn, DESY, TU Dortmund, FH Dortmund, Frankfurt, Freiburg, Heidelberg, KIT, Siegen, Göttingen, GSI, HLL-MPG WP 1.2 CMOS detectors for particle identification and energy measurement HU Berlin, Heidelberg, KIT, DESY		WP 2.1 LGAD sensors Darmstadt, DESY, Frankfurt, Göttingen, Hamburg, KIT, Mainz, GSI, HLL-MPG, MPP-MPG					
		WP 2.2 3D sensors Bonn, DESY, Freiburg, MPP-MPG WP 2.3 CMOS sensors with gain layers Freiburg, Heidelberg					
				WP3	Section System Integration	and Simula	tion
WP 3.1 Power management Aachen, FH Dortmund	WP3.2 Optical data transmission Wuppertal, FH Dortmund, KIT		WP3.3 sion 2.5D/3D integration KIT FH Dortmund, KIT, HLL-MPG				
WP3.4 Al strips on pCVD diamond carrier Frankfurt, GSI, Mainz	WP3.5 Reusability by on-detector intelligence FH Dortmund		WP3.6 ntelligence Radiation hardness and simulatio Frankurt, GSI, Hamburg, Heidelberg, KI				

![](_page_19_Picture_0.jpeg)

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![](_page_20_Picture_0.jpeg)

- CMOS detectors
  - integrate active sensors and readout on a single die
  - cost efficient, fast turnaround
  - lower material budget

![](_page_20_Figure_5.jpeg)

![](_page_21_Picture_0.jpeg)

CMOS SENSORS

- cost efficient, fast turnaround
- lower material budget

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many designs with good performance already exist

#### 10.1016/j.nima.2024.169428

![](_page_21_Figure_5.jpeg)

![](_page_21_Figure_6.jpeg)

![](_page_22_Picture_0.jpeg)

- Example for monolithic active pixel sensors
  - H2M: transfer from hybrid to monolithic
  - small feature size 65 nm process

**Digital circuit** 

DEEP P-WELL

1.25um

P- EPITAXIAL LAYER

P<sup>+</sup> SUBSTRATE

~10um

~30um

• ultrathin devices possible: 20 μm under study

LOW DOSE N-TYPE IMPLANT

NWELL

COLLECTION

ELECTRODE

35um

C. Lemoine, Pixel 2024

Analog circuit

P-WELL

N-WELL

DEEP P-WELL

![](_page_22_Figure_5.jpeg)

#### S. Ruiz Daza, https://doi.org/10.48550/arXiv.2502.06573 and Pixel 2024

![](_page_22_Figure_7.jpeg)

**Digital circuit** 

![](_page_23_Picture_0.jpeg)

• all-silicon modules

![](_page_23_Figure_2.jpeg)

#### Wafer with tested CMOS chips for the active part

requires high yield on wafer level

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Flex cable

700 µm wafer

all-silicon modules

Passive components

Thinned to 400 µm

industry and in-house production possible

# ALL SILICON MODULES

Dicing + fabrication of support Gluing and wire-bonding to support All-silicon ladder approach significant reduction in X/X<sub>0</sub> possible depends strongly on the requirements Dicing: 4-chip-ladders and single chips Post-processing of ladder candidates Sensor M. Vogt et al. CMOS Verbund Meeting 2024 ROC Glue Polymer RDL metal (Cu) post-processing Support & Cooling Vias Polymer Services bulk silicon metal pads (Al) Support structure ~1 mm single module material budget contributions illustrative example! Hybrid Module Thinned to ~40 um

![](_page_25_Picture_0.jpeg)

- ALICE ITS3
  - bent wafer-scale tracker with all-silicon modules

![](_page_25_Figure_3.jpeg)

#### A. Kluge, VCI2025

![](_page_25_Picture_5.jpeg)

bent wafer-scale tracker with all-silicon modules MAPS produced in TPSCo 65nm ۲ A. Kluge, good performance after bending & irradiation VCI2025 10rows) normal operating point 4.9° 447 64 99% efficient 10-4 383 9.7° - 14.6° əlbuə 319 - 255 or 19.5° t - 191 - 24.4° t - 24.4° t - 255  $10^{-3}$ 99.9% efficient (unassociated 99.99% efficient 127 29.2° 63 34.1° 22.4 °C - 39.0° 50 100 150 200 250 300 Threshold (e<sup>-</sup>)

ER1

ALICE ITS3 ۲

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## ALL SILICON MODULES

![](_page_27_Picture_0.jpeg)

- cost efficient instrumentation of large areas
  - passive CMOS strip sensors as a first step
  - prototypes produced using stitching •
  - good performance up to  $10^{15} n_{eq}/cm^2$
  - no visible impact of stitching

U. Parzefall. Si-D

March 2025

![](_page_27_Figure_6.jpeg)

	Low dolle delign 55 µm (20 strips)
1	

Reticle A	Reticle B	Reticle B	Reticle B	Reticle C

![](_page_28_Picture_0.jpeg)

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![](_page_29_Picture_0.jpeg)

### TIMING RESOLUTION

• temporal resolution of a silicon detector, neglecting subleading terms

![](_page_29_Figure_3.jpeg)

### 10.1016/j.nima.2016.05.078

![](_page_30_Figure_0.jpeg)

![](_page_31_Picture_0.jpeg)

- LGADs for future detectors?
  - increase fill factor for better efficiency

![](_page_31_Figure_3.jpeg)

![](_page_31_Figure_4.jpeg)

![](_page_31_Figure_5.jpeg)

![](_page_31_Figure_6.jpeg)

- goal for LGAD developments:
  - increase efficiency
  - maintain/improve spatial resolution
  - maintain/improve temporal resolution

![](_page_32_Picture_0.jpeg)

- trench-isolated LGADs
  - etched trenches are filled with SiO<sub>2</sub>, Si<sub>3</sub>N<sub>4</sub>, Polysilicon, ...
  - isolation of pixels  $\rightarrow$  better efficiency
  - successfull testbeam at DESY in 2024

![](_page_32_Figure_5.jpeg)

![](_page_32_Figure_6.jpeg)

## LGAD DEVELOPMENTS

trench-isolated LGADs

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- etched trenches are filled with SiO<sub>2</sub>, Si<sub>3</sub>N<sub>4</sub>,
  Polysilicon, ...
- isolation of pixels → better efficiency
- successfull testbeam at DESY in 2024
- resistive AC coupled silicon sensors
  - continuous gain layer
  - spatial resolution thanks to charge sharing
  - radiation effects on resistive layer under study
    - signal propagation in resistive layer not affected by radiation damage

![](_page_33_Figure_10.jpeg)

![](_page_33_Figure_11.jpeg)

![](_page_33_Figure_12.jpeg)

![](_page_34_Picture_0.jpeg)

### TIMING WITH 3D SENSORS

### • 3D sensors

- temporal resolution competitive with LGADs
- low depletion voltage
- possible issues
  - readout ASIC power
  - production (cost, yield)

![](_page_34_Figure_8.jpeg)

#### R. Koppenhöfer,

#### Si-D Consortium Workshop March 2025

![](_page_34_Figure_11.jpeg)

![](_page_35_Picture_0.jpeg)

# TIMING WITH 3D SENSORS

### • 3D sensors

- temporal resolution competitive with LGADs
- low depletion voltage
- possible issues
  - readout ASIC power
  - production (cost, yield)
- alternative electrode designs being studied
- trench-like electrodes tested up to FCC-hh like fluences
  - good timing resolution up to 10<sup>17</sup> n<sub>eq</sub>/cm<sup>2</sup>
  - setup limitations have impacted evaluation of samples at highest fluences

![](_page_35_Figure_12.jpeg)

![](_page_36_Picture_0.jpeg)

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![](_page_37_Picture_0.jpeg)

- use scintillators with silicon photon detectors: SiPM
- p-n junction operated in Geiger regime
- self-sustained avalanche due to strong electric field
  - quenching mechanism required

![](_page_37_Figure_5.jpeg)

![](_page_37_Figure_6.jpeg)

### <u>Phys.Med.Biol.65(2020)17TR01</u>

![](_page_38_Picture_0.jpeg)

- CALICE AHCAL prototype and beamtests •
  - 38 layers with 72x72 cm<sup>2</sup> active area each
  - 3x3 cm<sup>2</sup> cells, each 3mm thick •
  - steel as absorber (1.7cm)
  - power pulsing capable

![](_page_38_Figure_6.jpeg)

![](_page_38_Figure_7.jpeg)

![](_page_38_Picture_8.jpeg)

![](_page_38_Picture_9.jpeg)

K. Emberger, Precision Timing in Highly Granular Calorimeters and Applications in Long Baseline Neutrino and Lepton Collider Experiments

![](_page_39_Picture_0.jpeg)

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![](_page_40_Picture_0.jpeg)

### OTHER EXPERIMENTS AND CHALLENGES

### example for high rates: Lohengrin @ ELSA

- dark photon search in a fixed target setup
- 100 MHz single electron tracking
- low energy electrons in final state
- high occupancy in single pixels

![](_page_40_Figure_7.jpeg)

![](_page_40_Figure_8.jpeg)

### telescope like detector

high rate capable CMOS detector with minimal local services

![](_page_41_Picture_0.jpeg)

- material sciences: muon spin rotation
  - measurement of material properties
  - HV-MAPS as positron detectors
    - increase in muon rate
    - provide better spatial resolution

![](_page_41_Figure_6.jpeg)

![](_page_41_Figure_7.jpeg)

![](_page_42_Picture_0.jpeg)

#### https://the10ps-challenge.org/

# Annihilation

### • TOF-PET

- TOF-PET scanners using SiPMs since 2014
- 210 ps time resolution achievable
- push for 10-30 ps
- attractive candidate: digital SiPMs

![](_page_42_Figure_8.jpeg)

![](_page_42_Picture_9.jpeg)

#### <u>J Nucl Med. 2008 March ; 49(3): 462–470</u>

![](_page_43_Picture_0.jpeg)

- challenging detector R&D ahead in order to enable the full physics potential of future experiments
- future collider experiments:
  - material budget
  - granularity
  - radiation hardness
  - power management
  - data transmission
- other applications:

. . .

- non-collider experiments
- medical applications

- solutions meeting individual requirements already exist in many cases
- no single solution that meets all requirements for a given experiment yet
- many topics not covered in this presentation:
  - efficient power regulation on modules (DC/DC, Serial Powering)
  - in-silicon optical transceivers
  - CMOS LGADs

. . .