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Building Complex SoC Images Made Simple with SoCks

Modern System-on-Chip (SoC) devices are widely applicable; several boards in the LHC Phase-2 upgrade use them. However, their growing complexity, along with the increasingly intricate firmware and software development tools, makes it difficult for developers to keep up. To address this, we propose SoCks, a modular and scalable build framework for SoC devices that introduces a new layer of abstraction and reduces dependencies wherever possible while making the remaining ones comprehensible. Unlike previous approaches, SoCks enables independent builds of the firmware and software components of the SoC image, maintaining clearly defined interfaces to ensure the essential flow of information.

Speed talk:

I am unwilling/unable to present a speed talk

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