

First Major Release of 'fwk'

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Managing large-scale, long-lifecycle FPGA projects at scientific facilities demands a framework that is both flexible and stable. To meet this challenge, the MSK group at DESY has developed 'fwk', an open-source FPGA framework. While its initial implementation dates back to before 2015, 'fwk' has since evolved into a collaborative tool used by various scientific national labs and companies across Europe.

'fwk' provides a stable, EDA-tool-agnostic abstraction layer, enabling the seamless integration of IP modules. It can generate, implement, and perform verification targeting SoC FPGAs, including Yocto support.

This presentation will highlight the framework's key features, recent progress, and the significant milestone of the 1.0 release for the open-source scientific community.

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