

Overview of DMCS Projects and MicroTCA.4 Developments

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The Lodz University of Technology, Department of Microelectronics and Computer Science is involved in the development of MicroTCA.4, MicroTCA.4.1 and the future standards from 2007 onwards. Since that time, we have developed various MicroTCA.4 components including Intelligent Platform Management, Advanced Mezzanine Cards (AMCs), Rear Transition Modules (RTMs) for data acquisition and processing systems used in numerous accelerators and fusion projects.

The presentation discusses selected projects currently performed at our department based on the MicroTCA.4 technology.

Author: Prof. MAKOWSKI, Dariusz (Lodz University of Technology)

Presenter: Prof. MAKOWSKI, Dariusz (Lodz University of Technology)

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