Contribution ID: **27**   Type: **not specified**

Lab Exercise (EDK design flow)

Wednesday 14 March 2012 17:10 (50 minutes)

Creation and programming of an Xilinx MicroBlaze Embedded Processor
on Evaluation Board using Xilinx EDK Tools.
Creation and control of custom peripherals with the created processor.

Presenters: KRÜGER, Hans (University of Bonn); Mr HEMPEREK, Tomasz (University of Bonn)

Session Classification: FPGA School