

1st Meeting of the European XFEL Accelerator Consortium

Tuesday 17 April 2012

Timing, Electronics & FPGA development - SR 4a (16:00 - 18:00)

-Conveners: Holger Schlarb; Patrick Gessler

time	[id] title	presenter
16:00	[50] XFEL Timing System	HIDVEGI, Attila
16:15	[51] Electronic developments, concepts and applications for the machine	REHLICH, Kay
16:30	[55] MicroTCA.4 crate development for LLRF	JEZYNSKI, Thomasz
16:40	[52] Electronic developments and concepts for the LLRF	Dr LUDWIG, Frank
17:00	[54] Electronic developments, concepts and applications for the Experiments and Photon Beam Line DAQ	Mr GESSLER, Patrick
17:15	[56] FPGA Group, Framework developments and concepts	Mr GESSLER, Patrick
17:30	[57] Developments of the LLRF firmware also as basis for the start-up projects	Mr SCHMIDT, Christian