10Gb Ethernet and beyond on FPGA Workshop

Thursday 24 May 2012

<u>Implementation, Measurements and Experience Session</u> - Building 1 / Seminar Room 1 (15:30 - 17:30)

time	[id] title	presenter
15:30	[8] Ultra-fast streaming camera platform for scientific applications	Dr CASELLE, Michele
15:55	[9] FPGA-based Multi 10GbE Interface for the DSSC 2D Detector	Mr GERLACH, Thomas
16:30	[10] UDP/IP Implementation at DESY	SHEVIAKOV, Igor
16:45	[11] UDP/IP Implementation and Data streaming	HALSALL, Rob