

10Gb Ethernet and beyond on FPGA Workshop

Friday 25 May 2012

Interfacing on PC side Session - Building 1 / Seminar Room 1 (10:50 - 11:50)

time	[id] title	presenter
10:50	[16] High speed data recording - Hardware	Dr SZUBA, Janusz
11:15	[17] High speed data recording - Software	BOUKHELEF, Djelloul