

Advanced Techniques in LLRF control for XFEL - Collaboration Workshop

Tuesday 7 August 2012

Working Group - Main Auditorium A1/A2 (13:30 - 15:00)

time	[id] title	presenter
13:30	[43] Tutorial and discussion: how to use DSP elements available in FPGA	
13:35	[44] Discussion: documentation for LLRF systems	
13:40	[45] Discussion: problems with timing modules	
14:00	[49] Discussion: uTC resources and specification	