

Hoffman McLean Schroff

MTCA.4 Tutorial Basics Introduction in xTCA

MTCA Workshop for Industry and Research 11-12th December, 2012 DESY, Hamburg

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DESIGN WITH CONFIDENCE

Agenda

- What is xTCA?
- Specifications Overview
- ATCA Features
- AMC Features
- MTCA.0 Features
- MTCA.4
 - Initial Requirements
 - Mechanical Features
 - Board sizes
 - Management extensions compared to MTCA.0
 - Backplane
 - Keying
 - Cooling

What is xTCA?



ATCA

Advanced
Telecom
Computing
Architecture

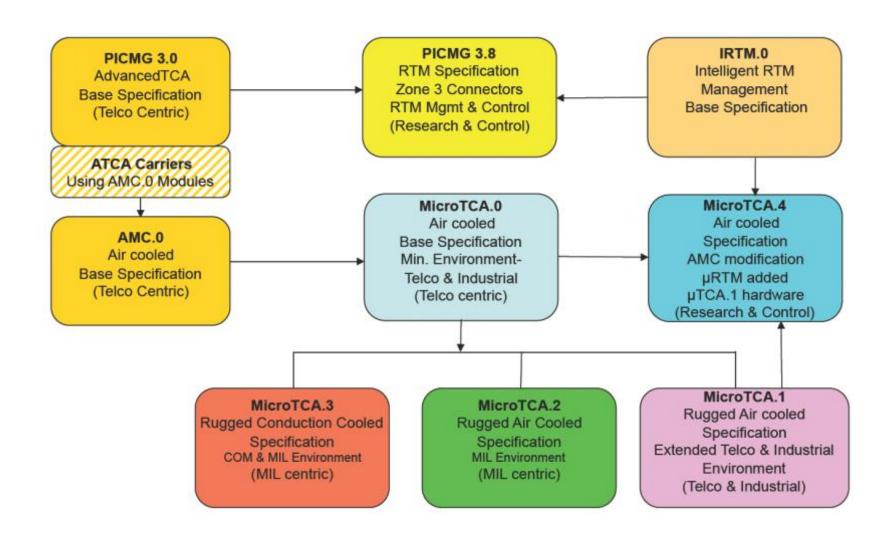
AMC

Advanced Mezzanine Card

MTCA

Micro
Telecom
Computing
Architecture

Standards



ATCA

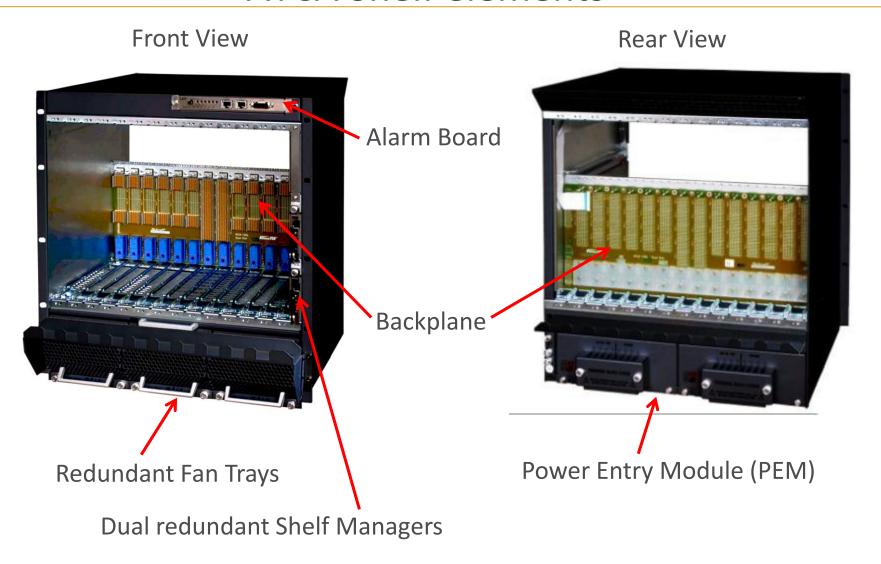
ATCA Features

- Sponsored by the PCI Industrial Computer Manufacturers Group (PICMG)
- Released in 2002
- Initially targeted to the Telco industry
- Typically DC powered (48V/60V)
- High Availability features (redundant modules, hot swap, etc.)
- High data throughput
- Remote management and monitoring
- However, features also of interest for other applications than Telco
- AC powered versions also available





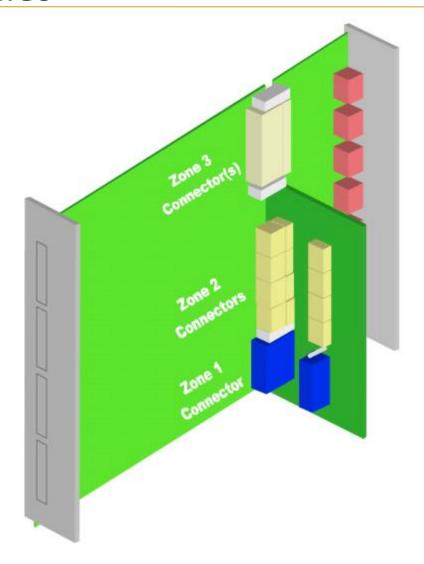
ATCA Shelf elements



ATCA Modules

Board size and connectors

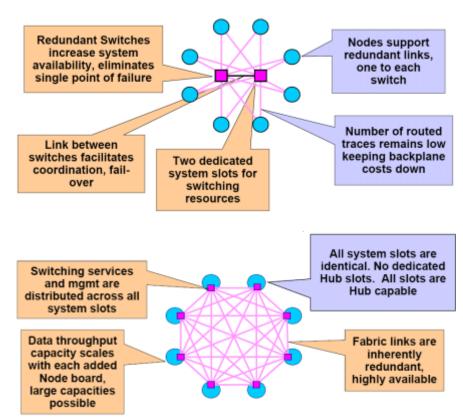
- Front board size 8U x 280
- Rear board (RTM) size 8U x 70 mm
 - Connects directly to front board
- Board width 6HP (1.2")
- Alignment/Key pins
- Zone 1: Management and Power
- Zone 2: Base Interface and Fabric Interface
- Zone 3: Interface to RTM



ATCA Interfaces

Zone 2 Backplane Interfaces

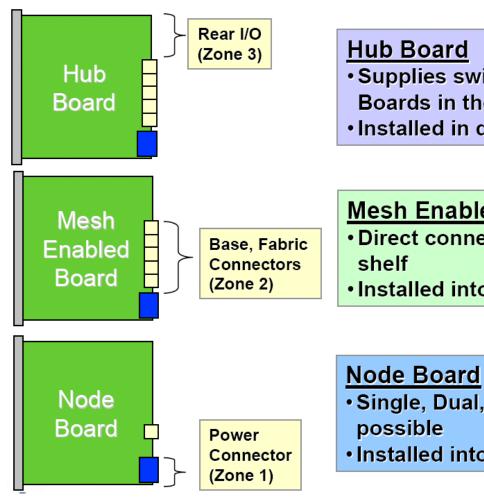
- Base Interface
 - 10/100/1000 BASE-T Ethernet
 - Always Dual Star topology
- Fabric Interface
 - Star topology
 - Mesh topology
- Clock Interface
 - Three dedicated clock interfaces
- Update Channel
 - Direct connection between two slots



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ATCA Modules

Board types



- Supplies switching connective Boards in the shelf
- Installed in designated Hub slots

Mesh Enabled Board

- Direct connection to all Boards in the
- Installed into any slot

- Single, Dual, Many Star connections
- Installed into any Node slot

ATCA Management

ATCA Shelf Management purpose

- Monitor & control low-level aspects of ATCA boards and other Field Replaceable Units within a shelf
- Watch over basic health of the shelf, report anomalies, take corrective action when needed
- Retrieve inventory information & sensor readings
- Receive event reports and failure notifications from boards and other intelligent FRUs
- Manage power, cooling & interconnect resources in the shelf (electronic keying)

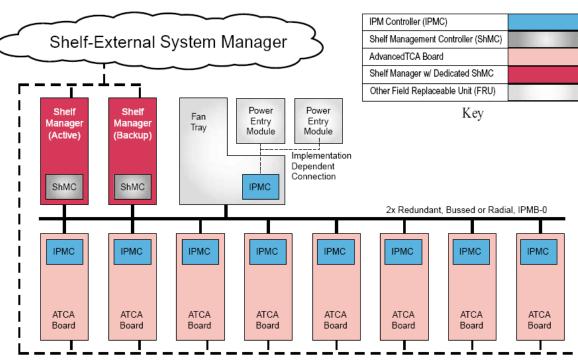


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ATCA Management

 Dedicated Shelf Management Controller (ShMC)

- ATCA Boards with IPMC
- Protocol IPMI (Physical layer I²C-Bus)
- Intelligent and Managed FRUs
- Bused or Radial IPMB



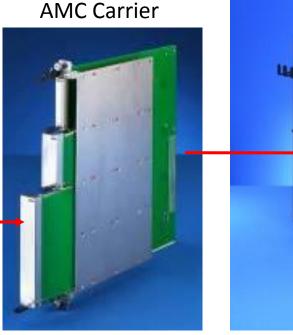
2x Redundant Radial Internet-Protocol-Capable Transport

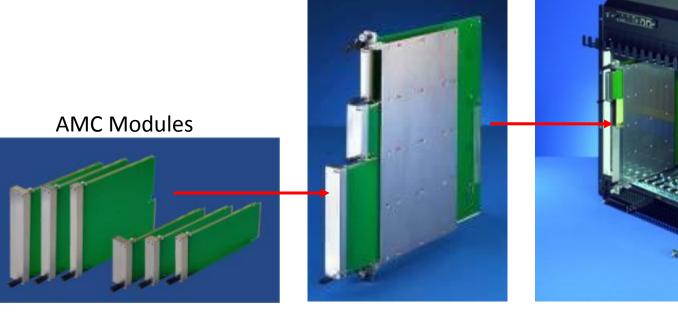
AMC

AMC Modules

- Initially developed as function extension for ATCA Boards
- Fully integrated into the ATCA IPMI management structure
- Plugged into a so called ATCA Carrier

Hot Swap capability







ATCA Chassis

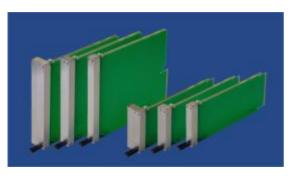
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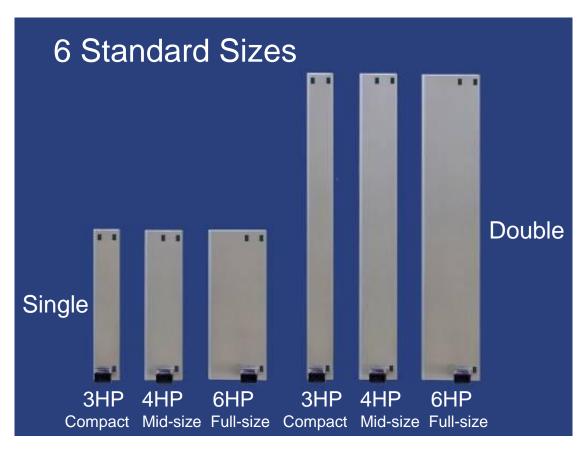
AMC Modules

AMC Module Sizes





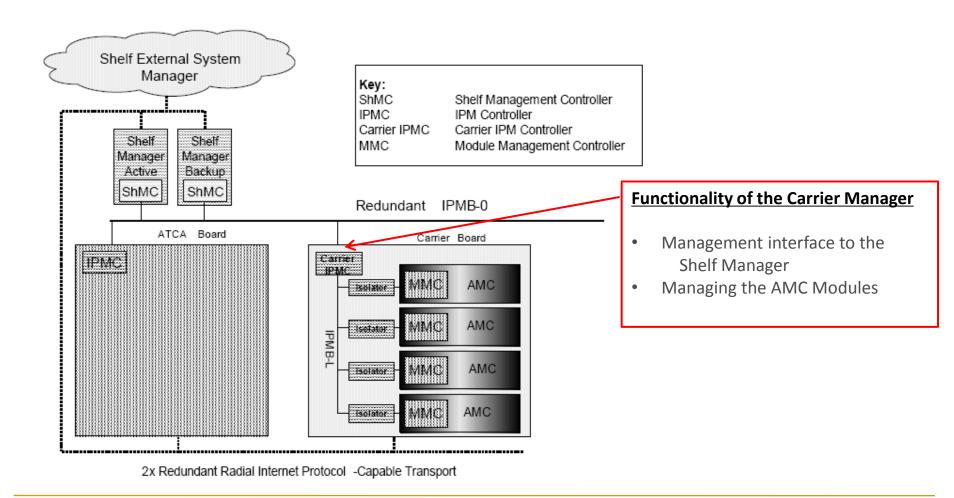
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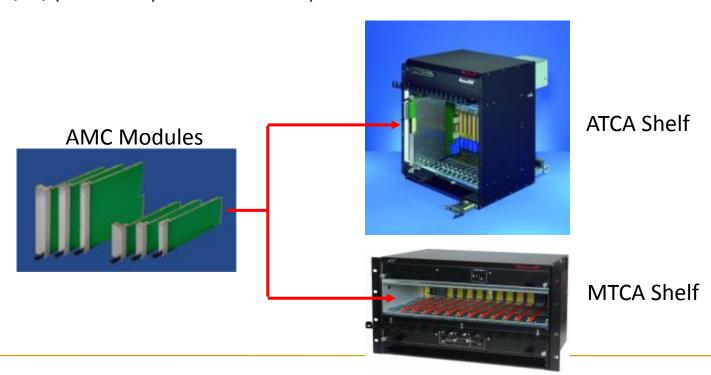
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AMC Modules

• Carrier IPMC represents the MMC on the AMC as a FRU to the Shelf Manager



- The basic idea of MTCA is to have a shelf that contains just AMC modules
- Backplane directly accepts AMC modules
- AMCs are interchangeable between ATCA and MTCA
- The infrastructure of a ATCA Carrier was adapted into the MTCA shelf (power, management, switching)
- No rear I/O, power input and all outputs to the front



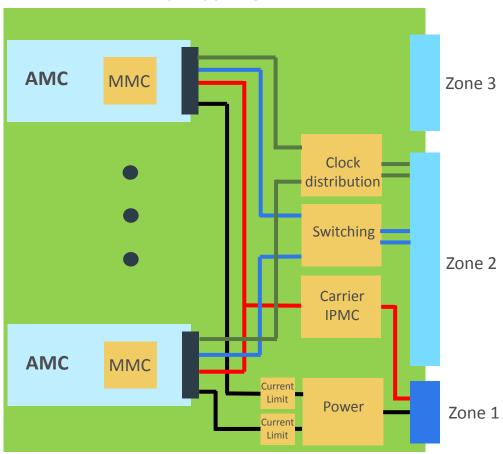
- As MicroTCA does not use a Carrier board, the power, management, clock distribution and switching functionality must be realized onto another device
- New Module: MCH (MTCA Carrier Hub)
 - IPMI management
 - clock distribution / generation
 - Switching functionality
 - JTAG slave / master
 - Redundant MCHs
- New Module: Power Module
 - 12V Payload Power

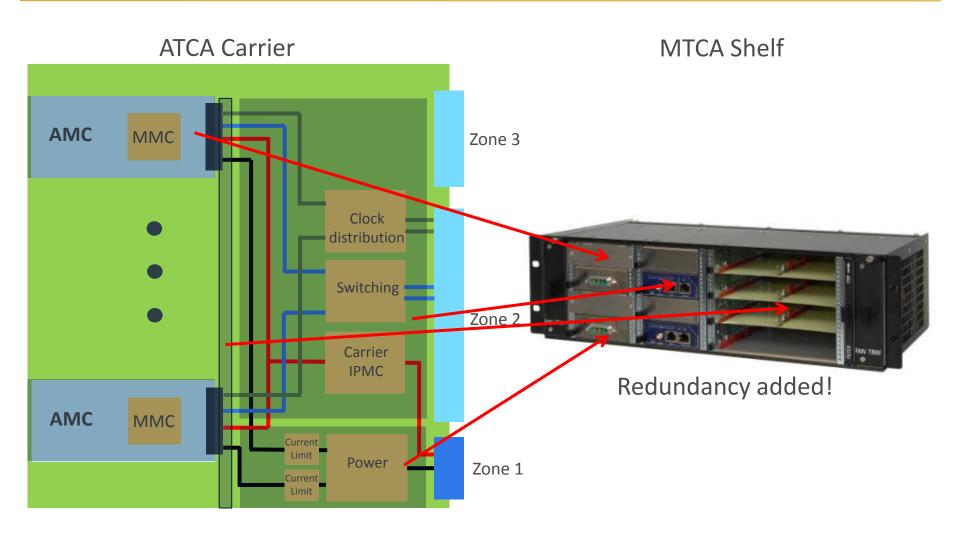
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- 3.3V Management Power
- Redundant power modules
- Special MTCA Shelf Slots for these new modules

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ATCA Carrier





Terms and Acronyms

- MCH MicroTCA Carrier Hub
 - This is the complete module you can buy from a vendor
- MCMC MicroTCA Carrier Management Controller
 - This is the physical IPMI controller on the MCH
- MMC Module Management Controller
 - This is the physical IPMI controller on an AMC
- EMMC Enhanced MicroTCA Carrier Management Controller
 - This is the physical IPMI controller on a Cooling Unit and on Power Module
- IPMB-0 Intelligent Platform Management Bus 0
 - Logical IPMB, physically divided into redundant IPMB-A and IPMB-B
- IPMB-L IPMB-Local
 - IPMI link between MCH and AMCs
- I²C-Bus Inter-Integrated Circuit Bus. Physical layer for IPMB

Why were extensions needed to the existing MicroTCA specifications?

No Rear Transition Module (RTM) defined for MicroTCA

 Physics applications typically require a large number of I/O cables. It makes sense to connect them to the rear of the chassis

Special clock and trigger topology

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 MicroTCA.0 specifies 3 Clocks and AMC.0 R2.0 specifies 4 Telecom and 1 Fabric Clock on the AMC Module. Physics applications typically need additional Clocks and Triggers

Sophisticated requirements for the clock and trigger accuracy

MicroTCA / AMC defines typical telecom clock signals corresponding to PCIe values. Trigger signals are not specified

Requirements for mechanics and sizes

- AMC Module size: Double, Mid-size
 - Allows for the max number of 12 AMCs in a 19" wide shelf
- Large MicroRTM real estate
 - MicroRTM size approximately the size of the AMC (doubles depth of existing uTCA chassis)
- Use front panel mechanics based on Rugged MicroTCA (MTCA.1)
 - Need to mechanically attach a module to avoid it being pushed-out by the corresponding module
 - Use Rugged MicroTCA retention device
- Reuse existing AMC front panels for the MicroRTM
- Allowing mounting of mezzanine modules on the rear of the backplane
- Optional zone 3 backplane
- Define the management of the system
- Suggest clocking and backplane topology

MTCA.0

Micro Telecommunications Computing Architecture Base Specification

MTCA.1

Air Cooled Rugged MicroTCA Specification

MTCA.2

Hardened Air Cooled MicroTCA Specification

MTCA.3

Hardened Conduction Cooled MicroTCA Specification

MicroTCA Specification Family Tree

MTCA.4

MicroTCA Enhancements for Rear I/O and Precision Timing

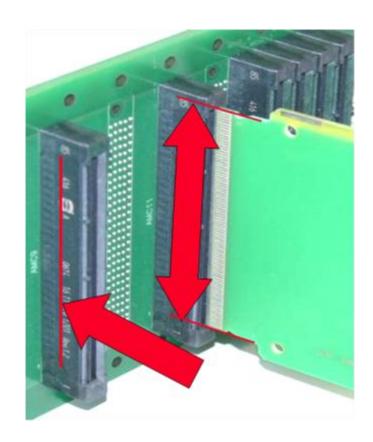
AMC.0

Advanced Mezzanine Card Base Specification

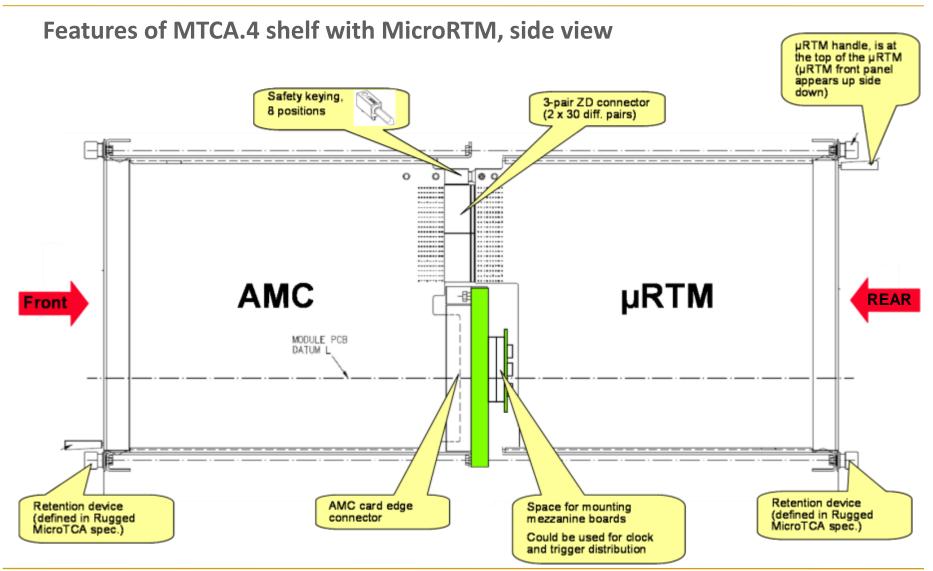
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Challanges when developing mechanics for the MicroRTM

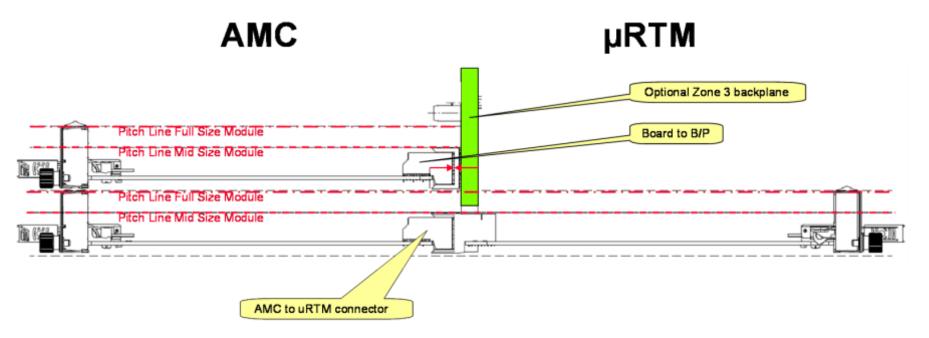
- The AMC card edge connector always needs to bottom for safe contact
- The AMC card edge connector does not allow vertical tolerances
- The AMC connector is the dominant connector. Additional tolerances caused by the MicroRTM must not interfere with mating of the AMC card edge connector



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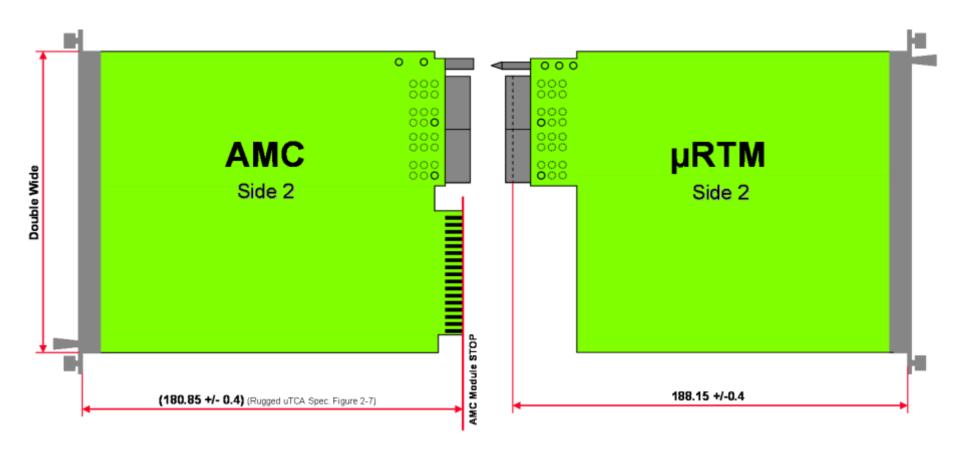


Features of MTCA.4 shelf with MicroRTM, top view



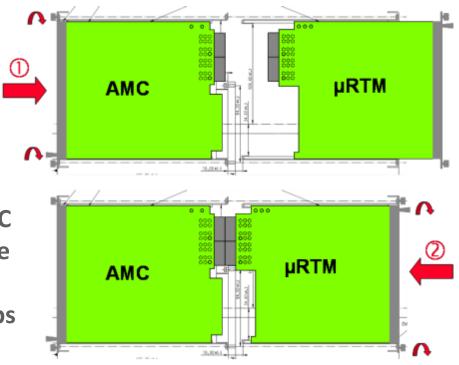
- The height of the module limits the connector usage
 - Full-size (6HP): 20 or 30 or 40 pair ADF connector
 - Mid-size (4HP): 20 or 30 pair ADF connector
 - Compact-size (3HP): no ADF connector possible

Module Sizes



Module Insertion Sequence

- The dominant role of the AMC card edge connector makes it necessary to follow an insertion sequence in order to avoid damage
- Step 1: Insert AMC and fix the retention device
- Step 2: Insert the μRTM and fix the retention device
- After this initial installation of the AMC and the μRTM one or the other may be removed as necessary (If both boards are removed follow again the two steps above)



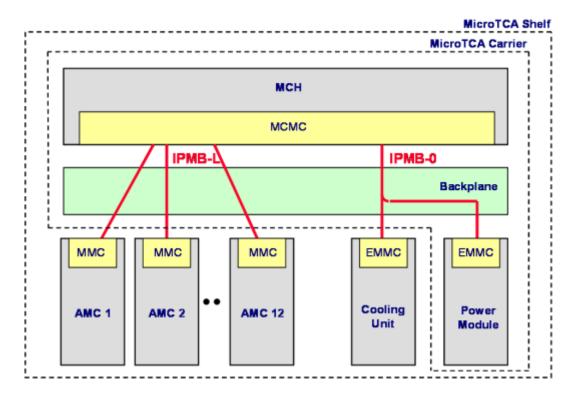
Management defined in AMC.0 / MTCA.0

• IPMB-L

- Connects the MCMC on the MCH to the MMC on the AMC Modules
- Radial architecture

• IPMB-0

- Connects the MCMC on the MCH to the EMMC on the PM and CU
- Bused architecture



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Management extensions in MTCA.4

• IPMB-L

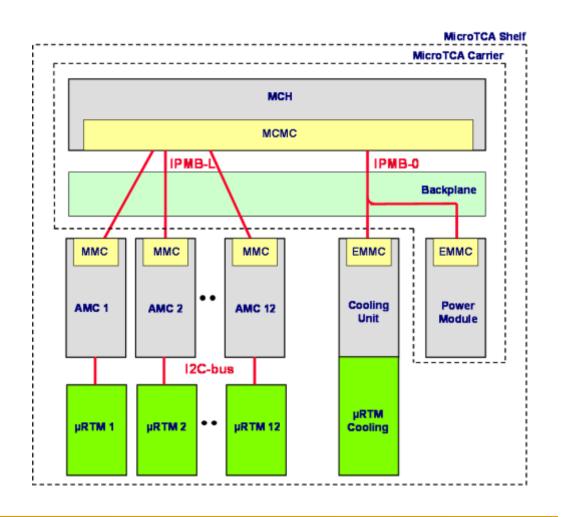
- Connects the MCMC on the MCH to the MMC on the AMC Modules
- Radial architecture

• IPMB-0

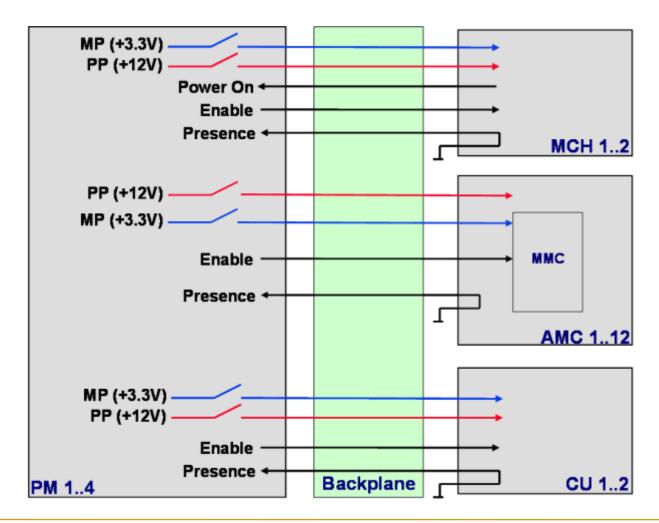
- Connects the MCMC on the MCH to the EMMC on the PM and CU
- Bused architecture

• I²C-Bus

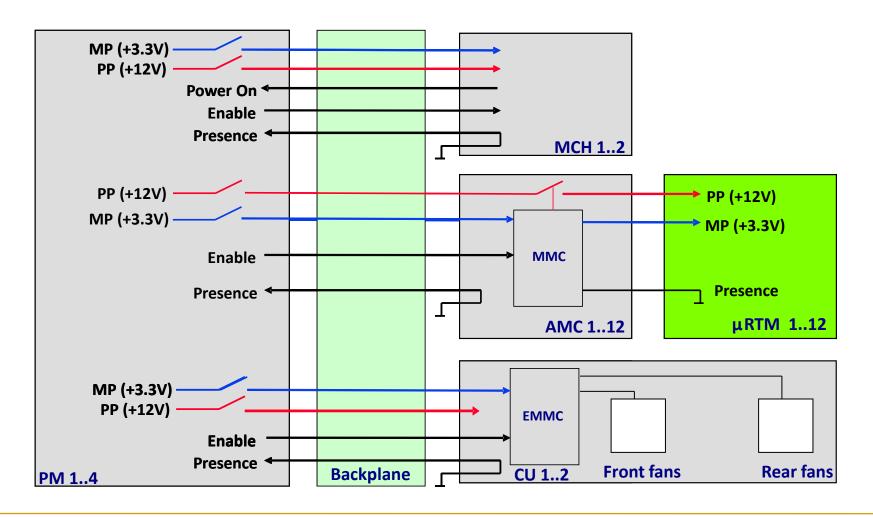
- Connects the AMC to the μRTM
- The μRTM is treated as managed FRU of the AMC



Control signals as defined per AMC.0 / MTCA.0



Additional RTM control signals for MTCA.4



Alignment and Keying

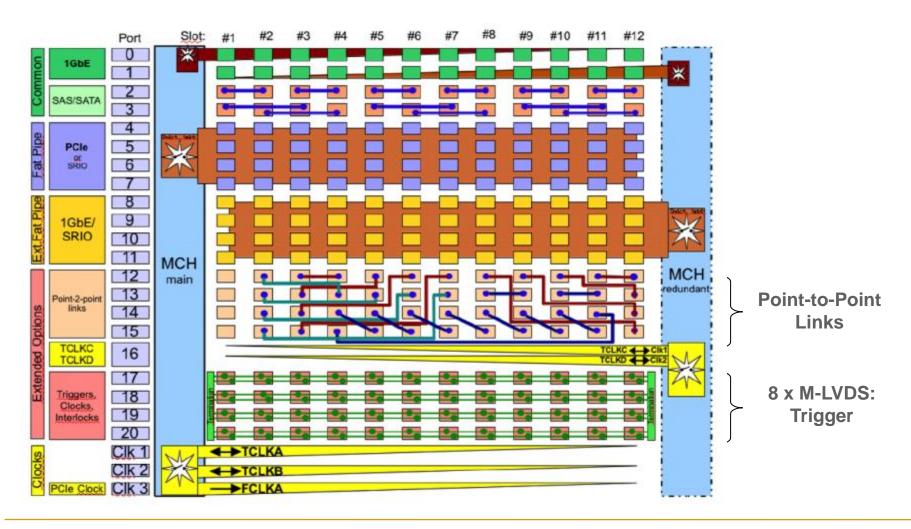
- mechanical keying prevents a module from being inserted which is not electrically compatible and could cause damage
- Eight keying positions are implemented that define the electrical interface

	A	View into rear	View into
N	Rotation in	of AMC	rear of µRTM
	degrees	Receptacle	Post
1	0	lacktriangle	
2	45	•	•
	45	•	
3	90	•	
	50	•	
4	135		
	100		•
5	180	•	
3	100		_
6	225		_
٥	223		
7	070	•	4
'	270		
8	315	•	
8	315	Ð	•
_			
0	NA		

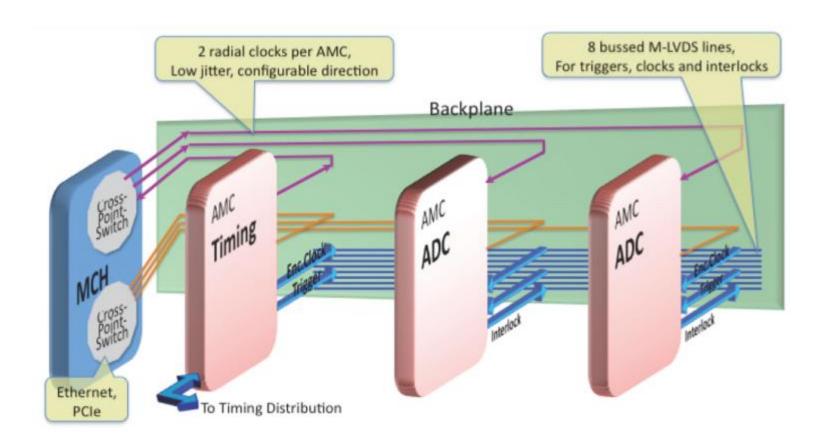
N	Data Signal in Volts		
1	LVDS		
2	0-±1		
3	>±1 - ±3.3		
4	>±3.3 - ±10		
5	>±10		
6	6 Reserved		
7	Reserved		
8	Reserved		



12-Slot MTCA.4 Backplane



Clock, Trigger and Interlock Signals



MicroRTM Management

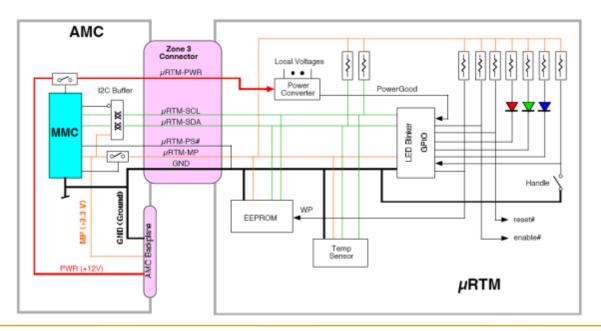
- A management interface is defined on the lower zone 3 connector
- Management and power signals:

μRTM-MP: Management Power for the EEPROM, Temp. Sensor and I/O Expander

 $-\mu$ RTM-PWR: Payload power for the RTM

μRTM-PS#: RTM Presence signal, grounded on the RTM

 $-\mu$ RTM-SCL/SDR: I²C bus coming from the AMC MMC going to the RTM



Shelf Cooling

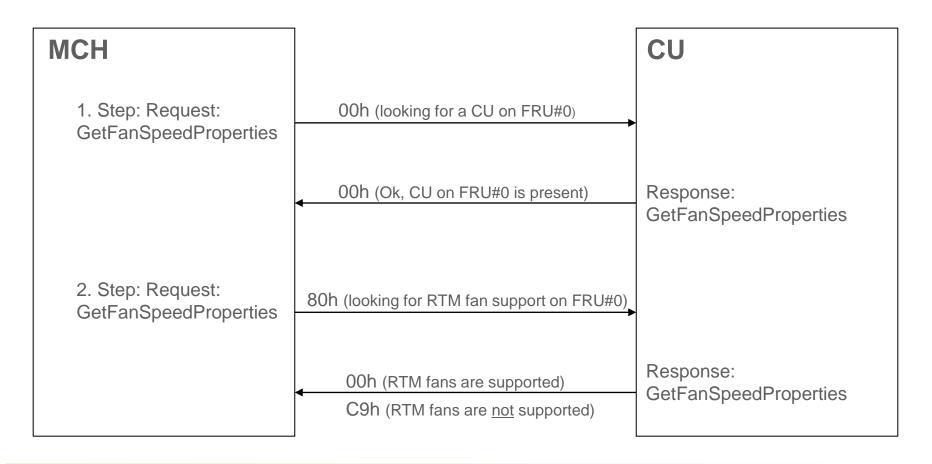
- Some Physics applications require independent cooling of the front boards and the rear boards due to thermally sensitive oscillators on the rear boards. Also, independent fan speed control contributes to lower noise and power consumption
- The fans for the front board and the rear board are on one physical Cooling Unit
- Existing specifications did not deal with independent fan speeds on one physical Cooling Unit
- MTCA.4 enhances the commands "Get Fan Speed Properties" and "Get/Set Fan Level" to include a bit telling the MCH whether the Cooling Unit supports independent cooling and telling the Cooling Unit whether a "Get/Set Fan Level"-command targets the front or the rear fans

MTCA.4 fan speed control commands

Data Type Data Field Byte Request Data PICMG Identifier. Indicates that this is a PICMG-defined group extension command. A value of 00h is used. If the Fan Tray does not support RTM Fan control: FRU Device ID. Indicates the FRU device for which the command is intended. Request sent by the MCH If the Fan Tray supports RTM Fan control: [7] - RTM Fan Speed Properties. This bit is set to 1b to indicate that the Get Fan Speed Properties query is for the RTM fans. This bit is set to 0b to indicate that the Get Fan Speed Properties query is for the front fans. [6..0] - FRU Device ID. Indicates the FRU device for which the command is intended. Response Data Completion Code PICMG Identifier. Indicates that this is a PICMG-defined group extension command. A value of 00h is used. Minimum Speed Level. This field describes the minimum setting that is accepted by the Set Fan Level command. Maximum Speed Level. This field describes the maximum setting that is accepted by the Set Fan Level command. Response sent by the CU Normal Operating Level. This field represents the default normal fan speed recommended by the fan manufacturer. Fan Tray Properties. This field holds properties of the Fan Tray. [7] - Local Control Mode Supported. This bit is set to 1b if the Fan Tray supports automatic adjustment of the fan speed. [6:0] - Reserved

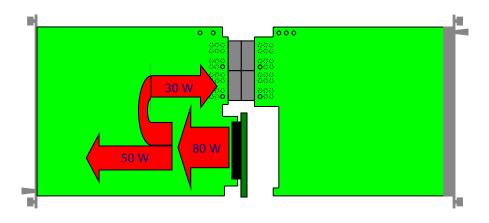
Table 3-9: MTCA.4 Get Fan Speed Properties

Sequence of IPMI commands to determine whether the CU supports independant MicroRTM cooling



Front board and MicroRTM power requirements

- The total power for a slot (front board and RTM) is supplied through the front board AMC connector
- The MicroRTM power is supplied from the front board through the Zone
 3 connectors
- Total available power for a slot is 80 Watts, the MicroRTM power is limited to 30 Watts
- The power required by the MicroRTM is subtracted from the power for the front board



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Thank you

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