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A hadron calorimeter for future ILC optimized for particle flow



Sebastian Laurien

- ILC
- Hadron calorimetry and particle flow
- AHCAL: analogue hadron calorimeter
- Test beam at CERN with the technological prototype

Next Collider - ILC



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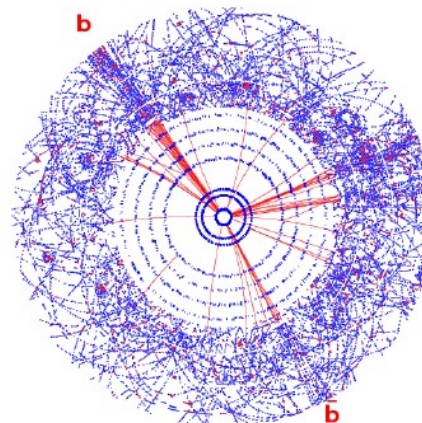
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ILC:

- next generation lepton collider
- clean environment
- 500GeV / 1TeV
- high luminosity
- **precision** studies/ measurement

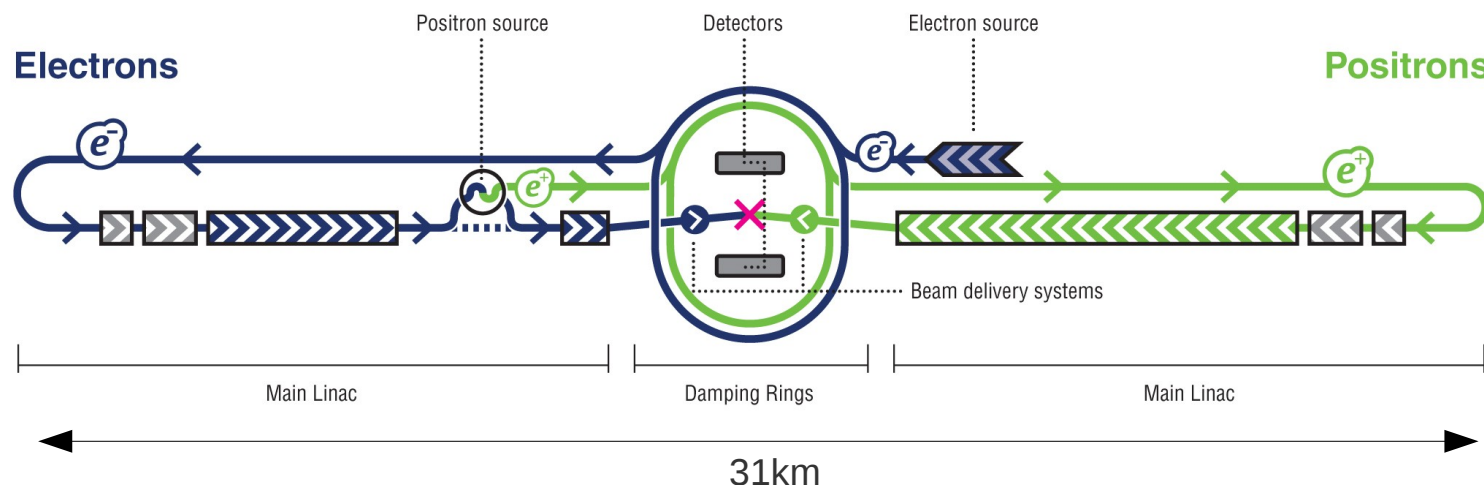
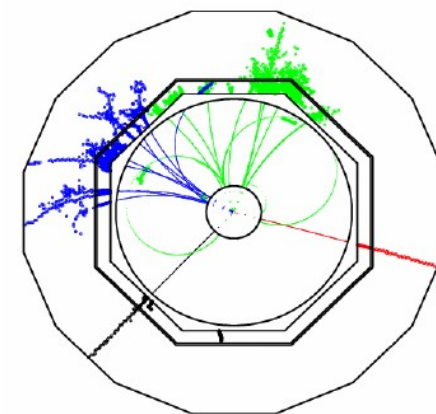
The LHC

$pp \rightarrow H + X$



The ILC

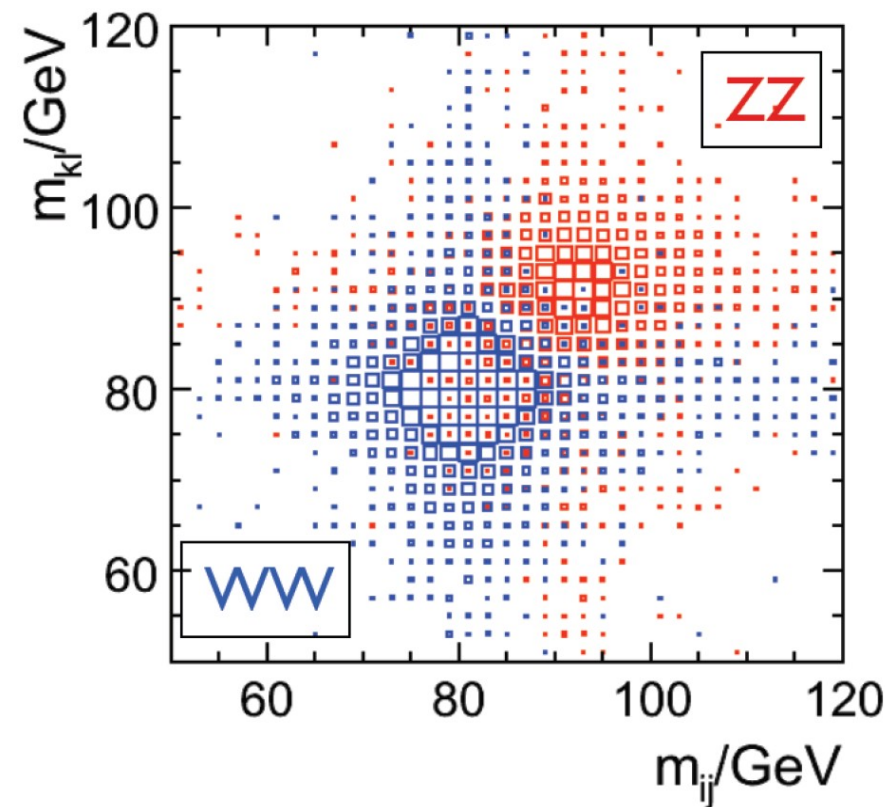
$e^+e^- \rightarrow HZ$



ILC physics: separation of hadronic W, Z decays

→ need high jet energy resolution : $\frac{\delta E_{jet}}{E_{jet}} = \frac{30\%}{\sqrt{E}}$

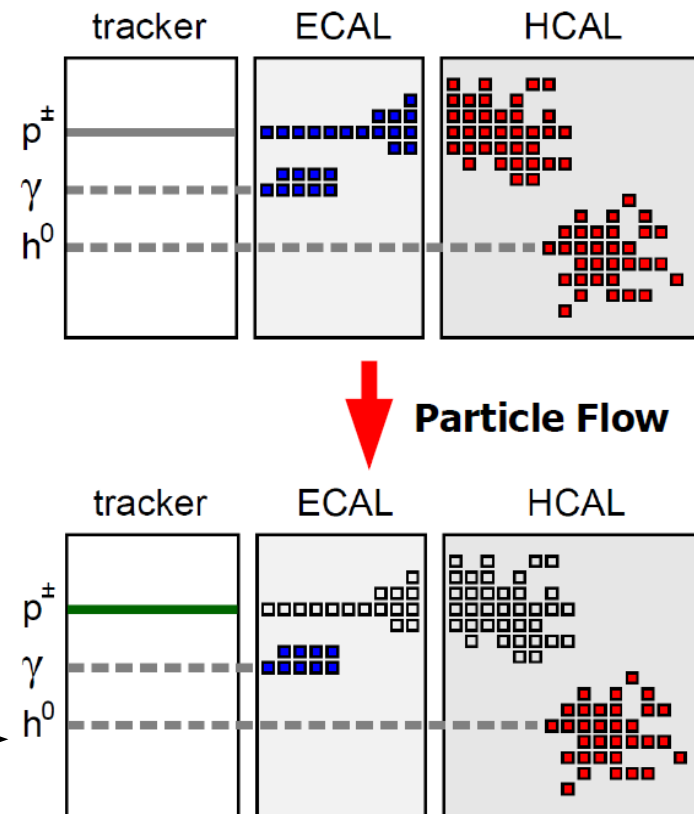
→ particle flow : choose detector best suited for particular particle type



Typical jet energy composition:
60% charged hadrons
30% photons
10% neutral hadrons

$$\sigma_{jet} \approx f_{\pm} \cdot \sigma_{tracker} \oplus f_{\gamma} \cdot \sigma_{ECAL} \oplus f_0 \cdot \sigma_{HCAL} \oplus \sigma_{confusion}$$

measured by HCAL



→ Build the best possible calorimeter in order not to use it.

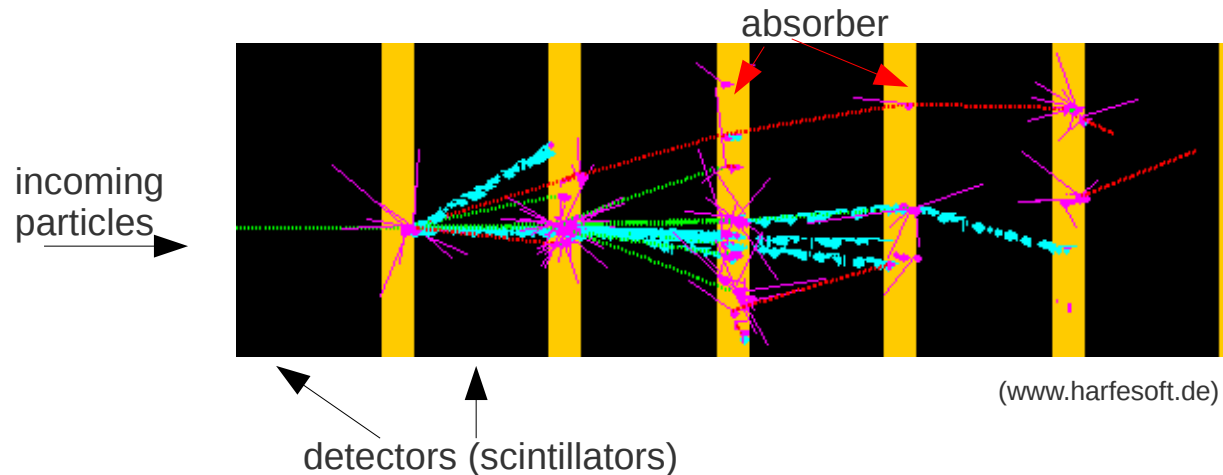
Hadron Sampling Calorimeter



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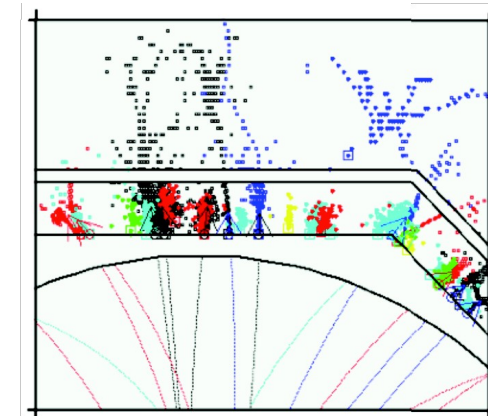
- measures the energy of a neutral hadron → need to stop particles: very dense absorber (Steel)
- only fraction of the calorimeter is sensitive volume (detectors)



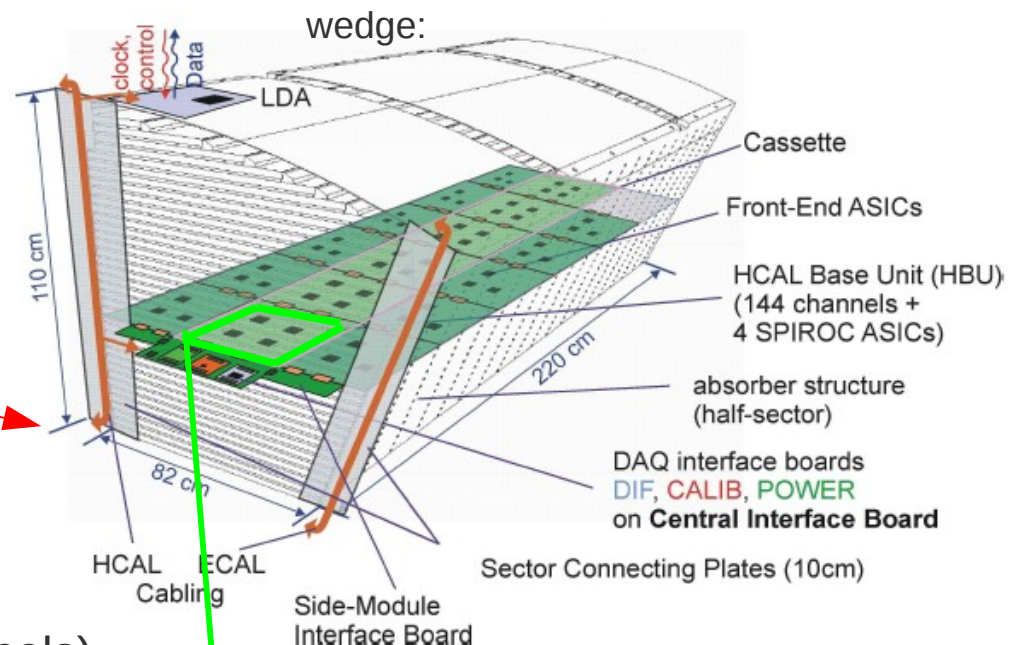
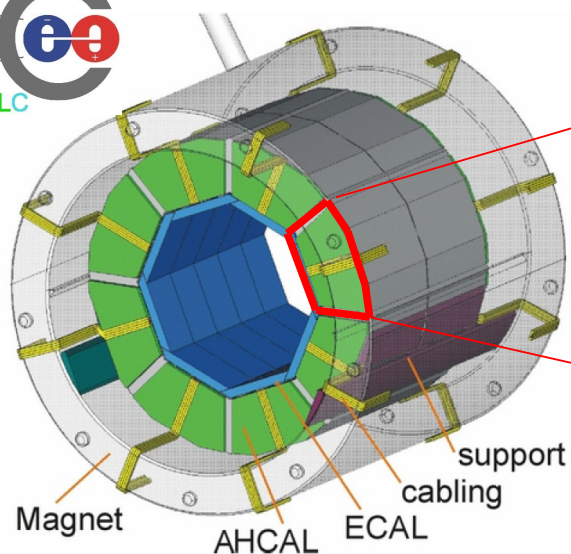
sensitive layers read out with scintillators and light detectors.

HCAL for ILC:

- highly segmented for particle flow (separate energy deposits in calorimeter)
- 48 layers ($\sim 6\lambda$)
- $\sim 10^6$ channels
- integrated readout
- low power consumption (avoid active cooling)
- time information
- HCAL inside magnetic field



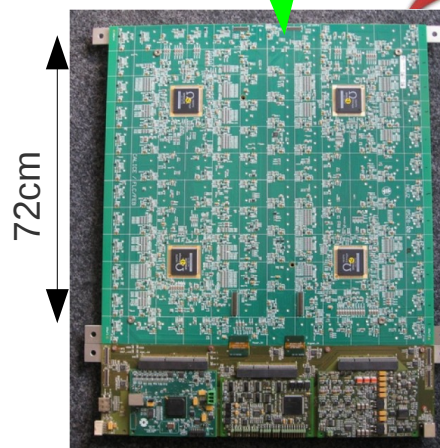
The AHCAL (analogue hadron calorimeter)



The analogue hadron calorimeter:

- modular, smallest hcal base unit (144 channels)
- 48 cassettes in one wedge
- $3 \times 3 \times 0.3 \text{ cm}^3$ plastic scintillator
- fully integrated readout and ASICs
- power pulsing ($25 \mu\text{W}$ / channel) (not needed electronics switched off in parts of a cycle)

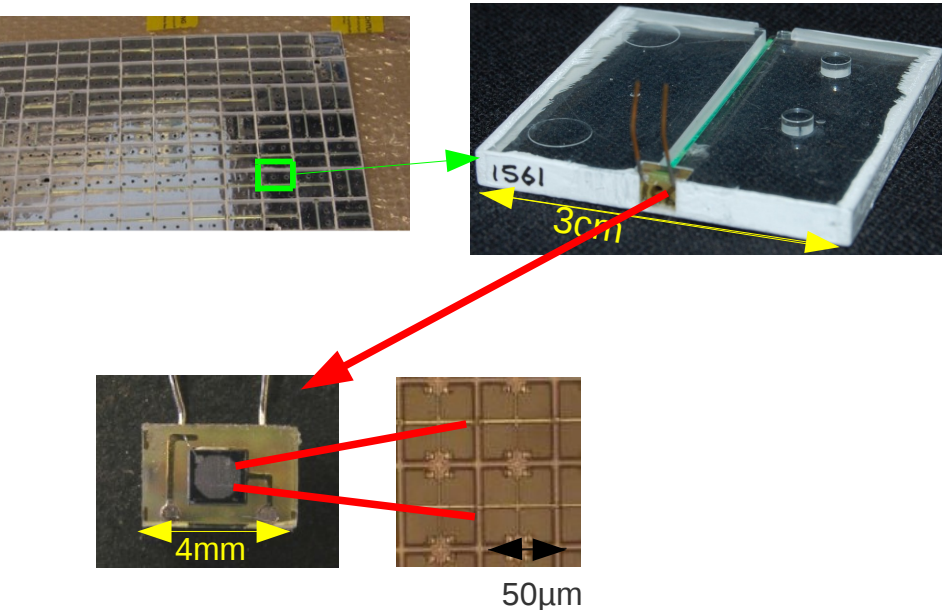
HBU with ASICS



scintillating tiles mounted on the other side



plastic scintillator tile



Readout of tiles with silicon photo multipliers (SiPM) mounted directly on HBU

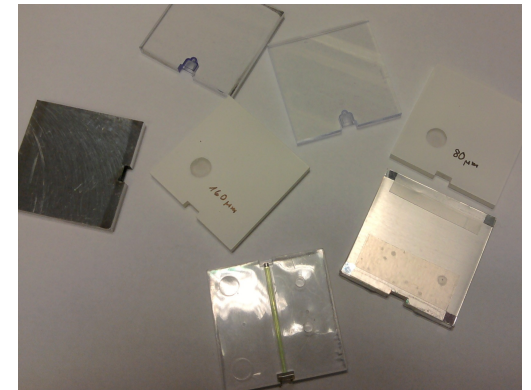
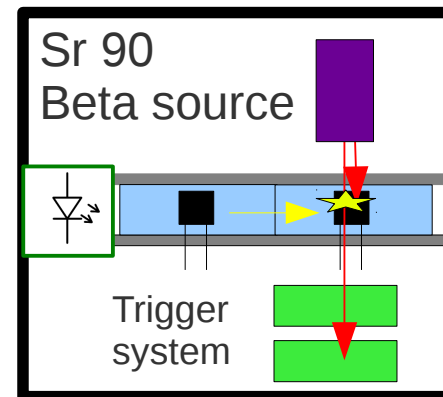
SiPM:

- insensitive to magnetic fields
- array of ~1000 APD pixels in Geiger mode
- every fired pixel gives a fixed charge
- analogue signal: sum of all pixels
- single photon sensitivity

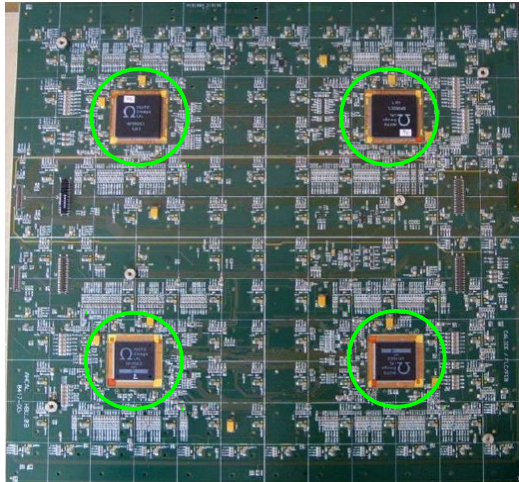
Up and running setup for scintillator tile and SiPM characterization:

- response to minimal ionizing particles (MIP)
- temperature and voltage dependencies
- investigation of different SiPMs and tile coating / wrapping

clima box



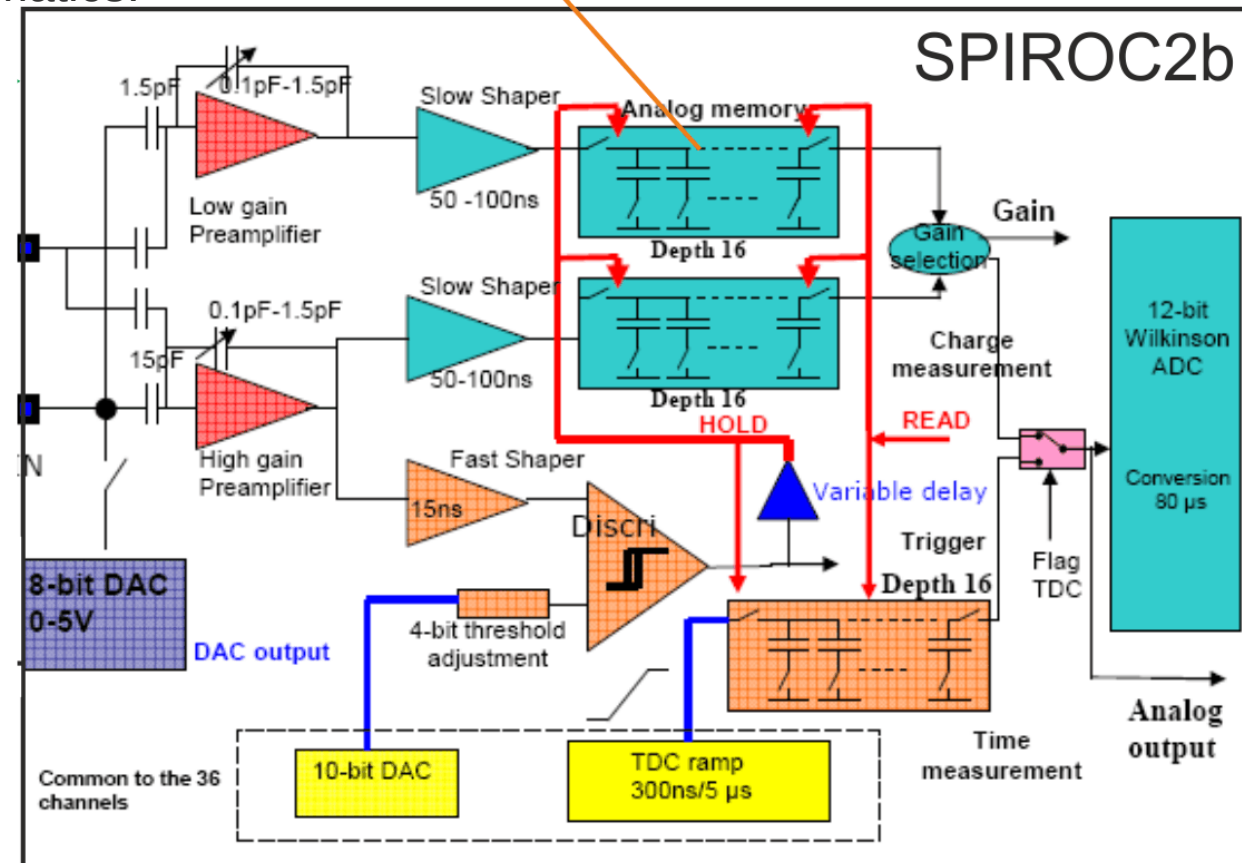
The readout chip



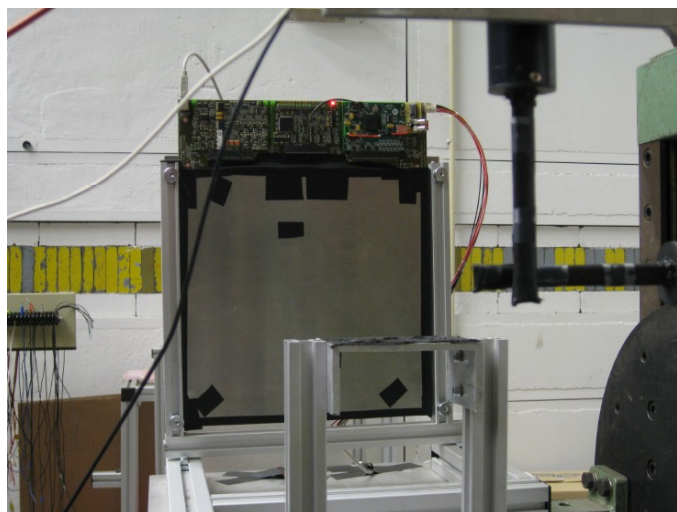
- 4 ASICs on one base unit:
- optimized for ILC operation
 - 36 channels
 - low power consumption ($25\mu\text{W}/\text{ch}$)
 - dual gain ADC (adjustable)
 - **auto trigger** with adjustable threshold
 - timing measurement (resolution ILC mode **100ps**)

single channel schematics:

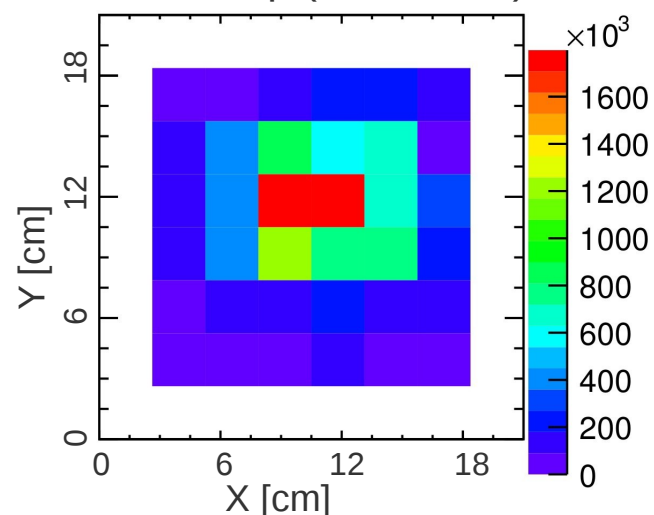
16-stage analogue memory



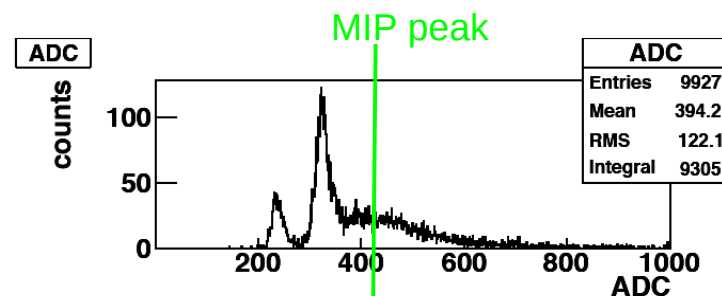
base unit in DESY test beam:



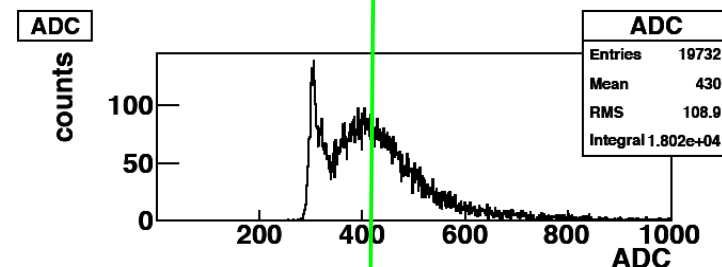
Shower in one chip (3GeV, $2X_0$) sum ADC



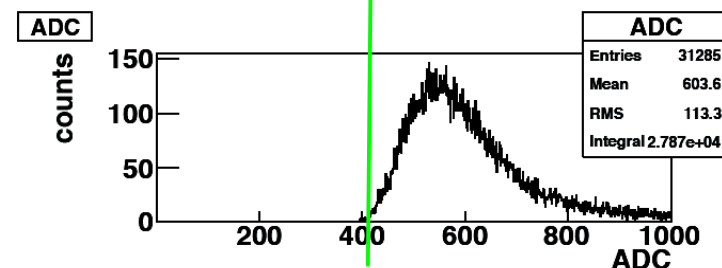
Minimal ionizing particles (MIP) in DESY test beam for calibration of auto trigger threshold



too low threshold



good threshold



too high threshold

The AHCAL – CERN test beam



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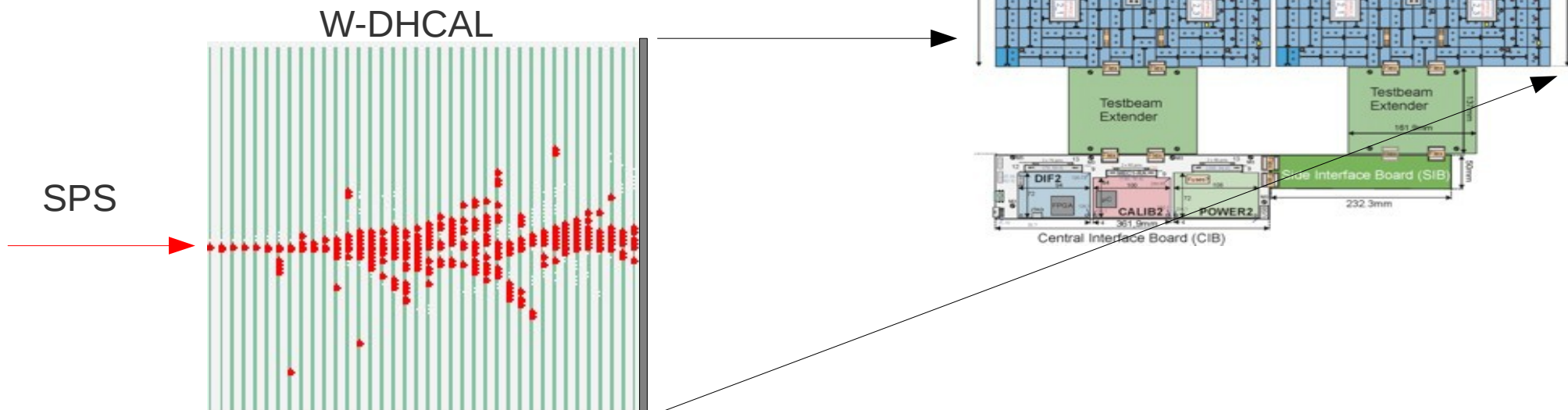
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The technological prototype: one layer with 4 HBU:

- 576 channels
- same electronics and readout as for ILC
 - 16 independently auto triggered chips

AHCAL technological prototype will go in test beam :

- SPS at CERN (pions) in November
- downstream the tungsten digital HCAL (W-DHCAL) after 4λ
- time resolution **1ns** (test beam mode)



Hadron test beam goals



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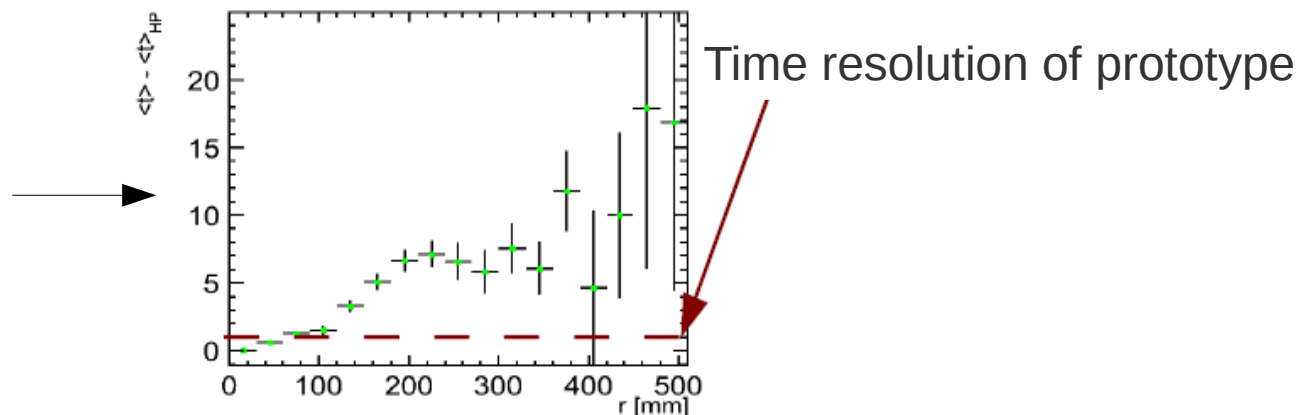
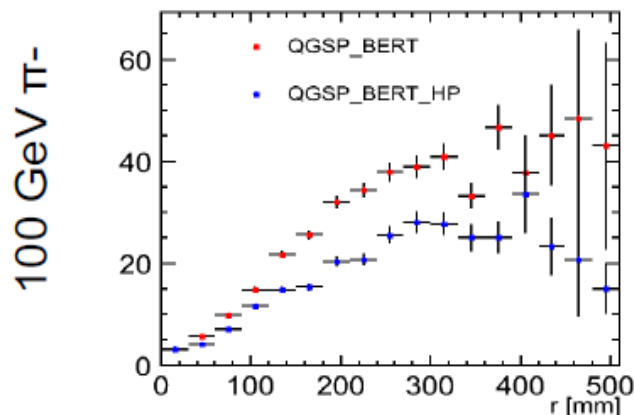
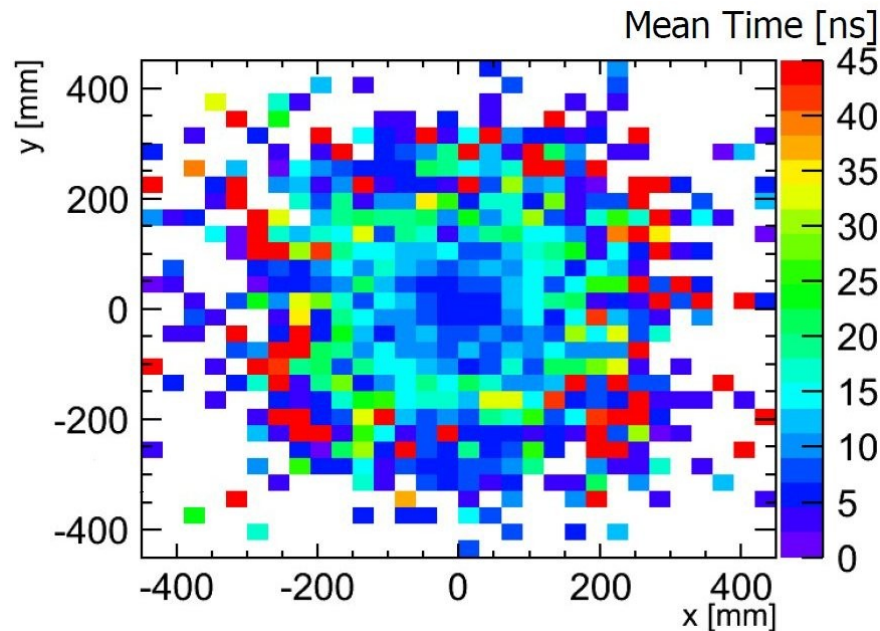
Main goal:

establish electronics and readout in a realistic detector scenario

Physics goal:

- detailed measurements of Hadronic showers
- test and validation of different physics simulations
- resolution **1ns**

→ e.g. radial distribution of mean time of hit in the last layer



- CALICE collaboration is developing particle flow calorimeters for linear colliders
- AHCAL technological prototype:
 - One layer with 4 AHCAL base units (HBU) in hadronic test beam Nov.2012
 - test of the integrated readout electronics
 - investigation of hadronic shower time development



Thank You!

For your attention

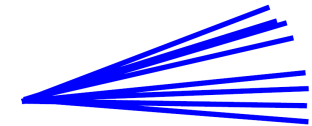
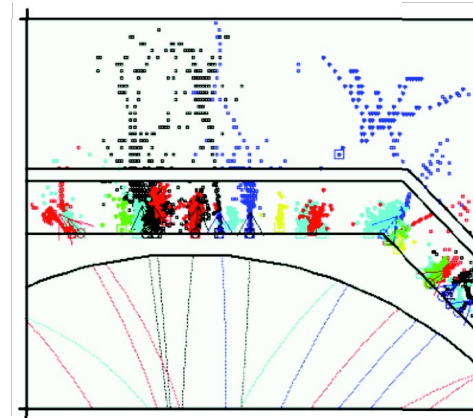
ILC detectors need to be optimized for precision measurements in difficult multi-jet environment

Traditional approach

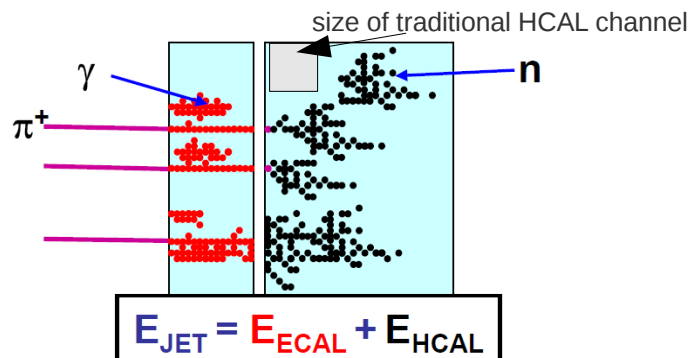
- energy measured by ECAL + HCAL
- poor HCAL resolution limits jet resolution

Particle Flow (PF) approach

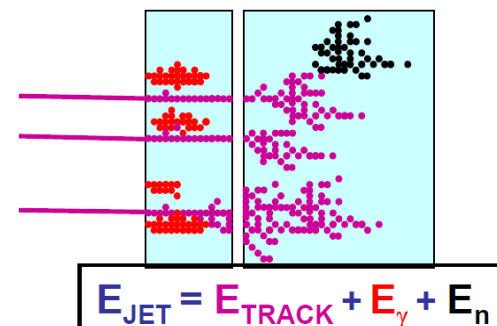
- measure every particle in the device with best resolution
- need to assign energy deposit in calorimeter to single particles (tracks)
- only neutral hadrons measured in HCAL (only 10% of jet energy)



Typical jet energy:
60% charged hadrons
30% photons
10% neutral hadrons



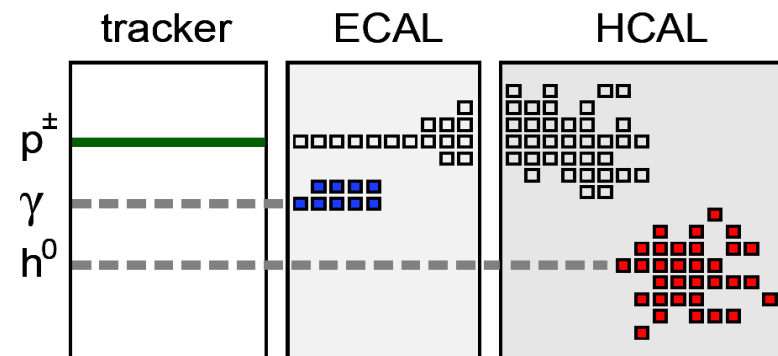
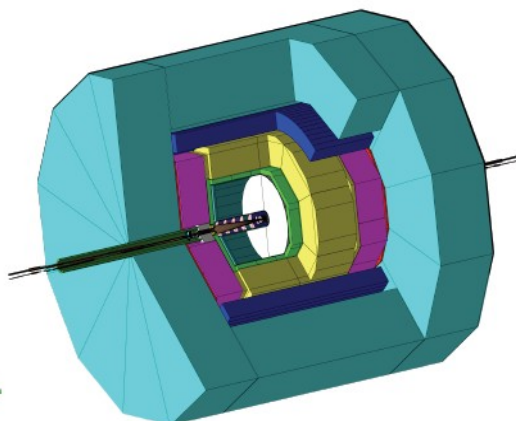
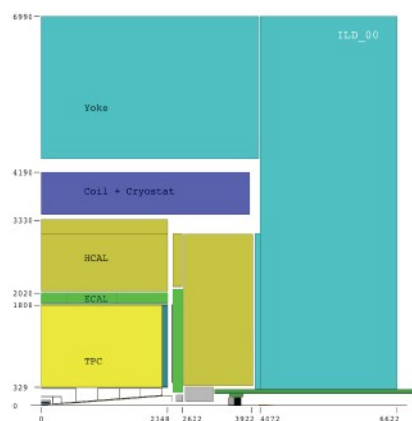
Traditionell approach



PF approach

PF HCAL: Build the best possible calorimeter in order not to use it.

The Energy resolution of the ILD



Archive better Energy resolution through the Particle Flow Algorithm (PFA) approach:

- Energy of **charged particles** measured by tracking detectors
- Energy of **photons** measured by Electromagnetic Calorimeter (ECAL)
- **Neutral particles** energy measured by the Hadronic Calorimeter (HCAL)

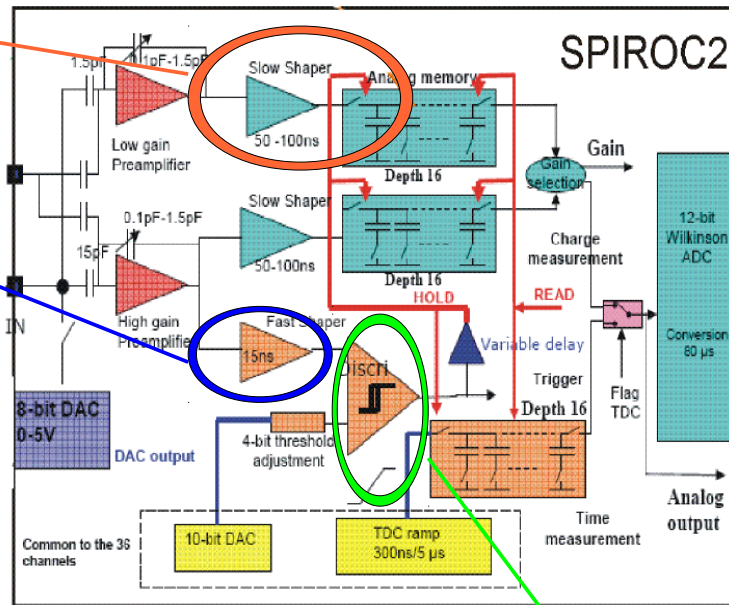
$$\sigma_{\text{jet}} \approx f_{\pm} \cdot \sigma_{\text{tracker}} \oplus f_{\gamma} \cdot \sigma_{\text{ECAL}} \oplus f_0 \cdot \sigma_{\text{HCAL}} \oplus \sigma_{\text{confusion}}$$

To reduce the confusion term of Energy resolution, an high granularity calorimeter is needed to precisely reconstruct the final states of the jet

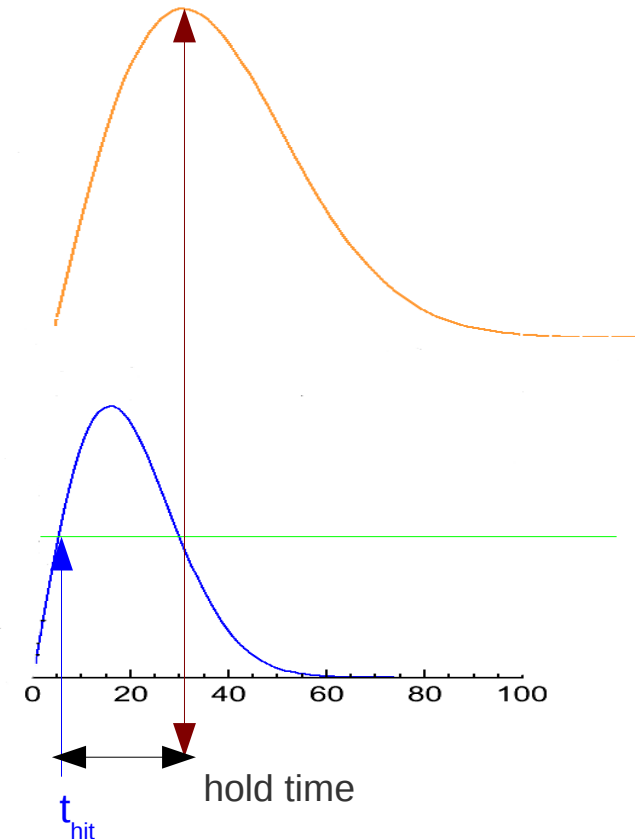
Time stamping: the ASIC

Slow shaper

Fast shaper



Threshold discriminator



Hit signal splitted:

- A **fast shaper** (~ 25 ns shaping time) feeds a **threshold discriminator**;
- A **slow shaper** (~ 50 ns) feeds the analog memory;

Whenever **fast signal** amplitude passes the **threshold**:

- Time information is stored (of threshold passing);
- Amplitude of **slow signal** at hold time is stored;

Time stamping: digitization

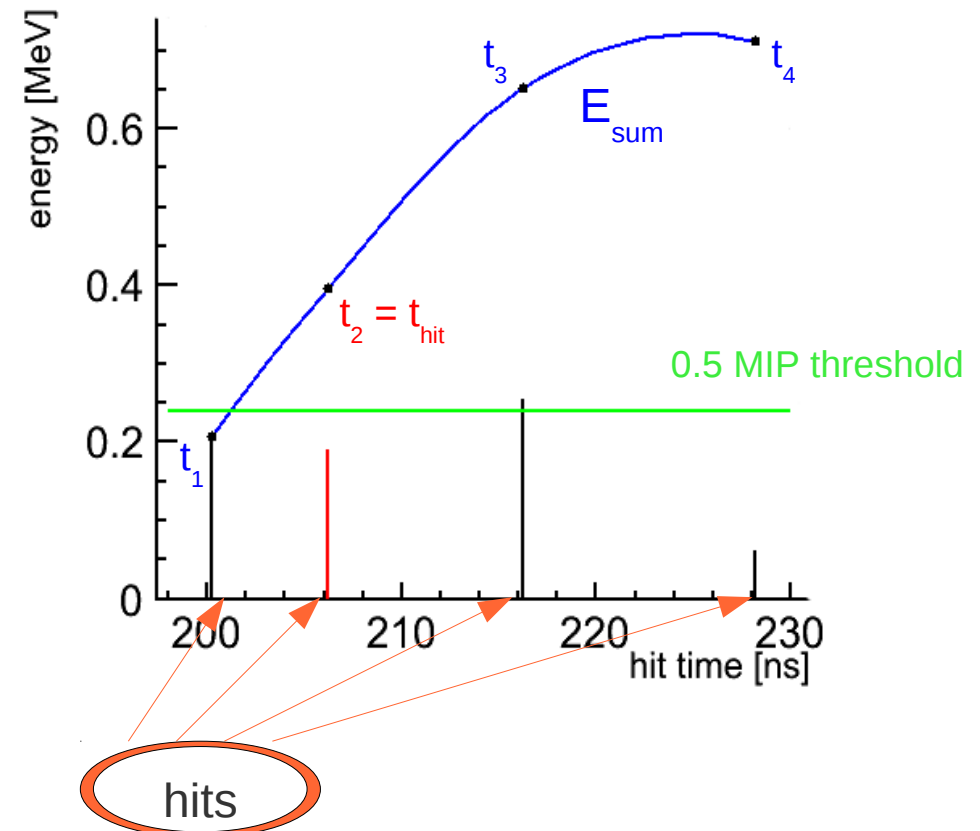
For each cell several hits characterized by:

- A time value
- A deposited energy value

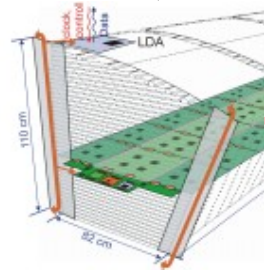
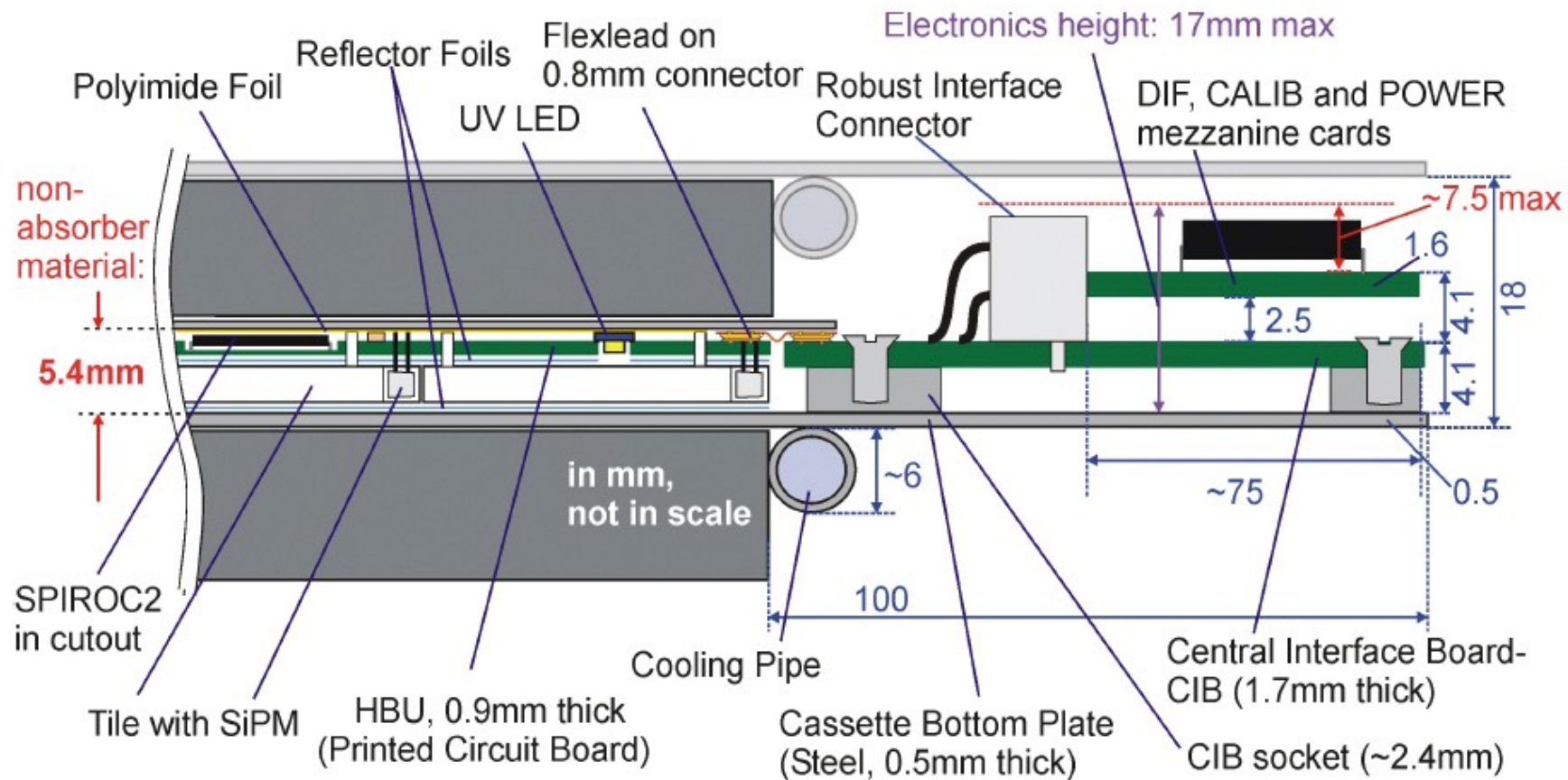
To simulate ASIC behavior, for each Cell:

- hits are temporally ordered
- hit energy is added until $t_i = t_1 + 25 \text{ ns}$
- $t_{\text{hit}} = t_i$ first hit passing threshold

- $E_{\text{sum}} = \text{sum of } E_i \text{ until } t < t_{\text{hit}} + t_{\text{hold}} \quad t_{\text{hold}} \sim 50 \text{ ns}$



No ASIC noise or time jitter had been considered yet ...



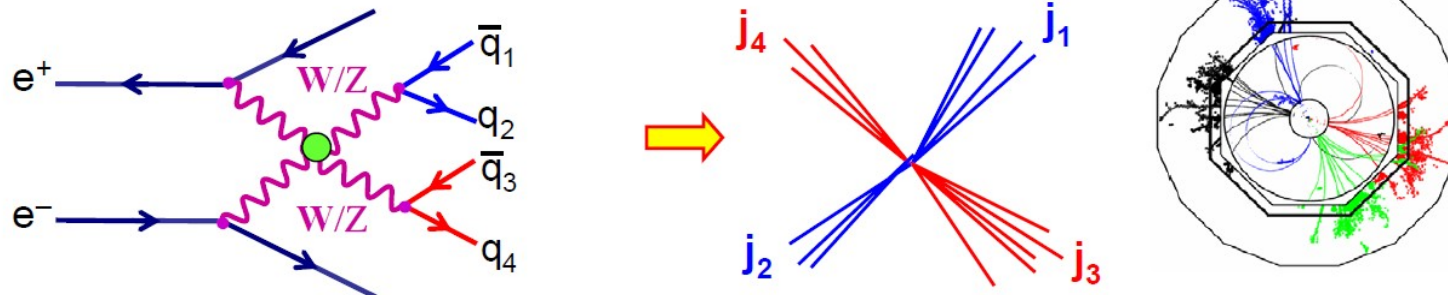
Abbr.	Name
DIF	Detector Interface Board
CALIB	Steering for LED calibration
CIB	Central Interface Board
HBU	Front-end board

- Compliant with **steel and tungsten** options
- Redesign and production of components almost finished

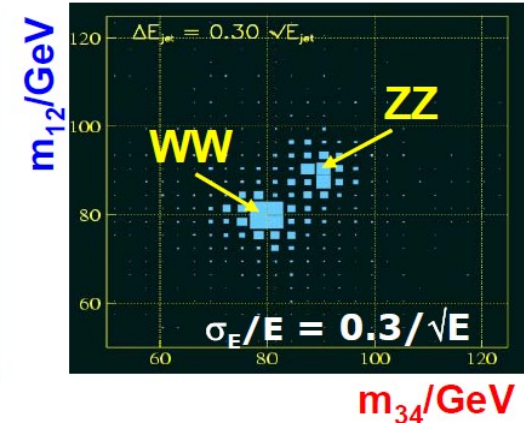
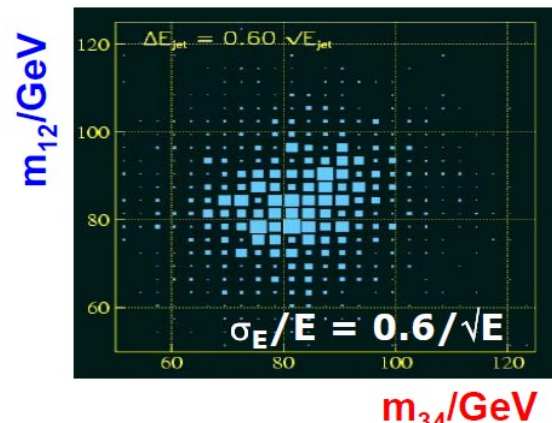
Linear Collider Calorimetry

- ★ Any future collider experiment geared towards precise measurements requires very good jet energy resolution to maximise physics reach:

Often-quoted example at ILC: $e^+e^- \rightarrow \nu\bar{\nu}W^+W^-$ vs. $e^+e^- \rightarrow \nu\bar{\nu}ZZ$



Reconstruction of two di-jet masses discriminates between WW and ZZ final states



First generation physical prototype of the AHCAL:

→ proof of principle for particle flow

but also:

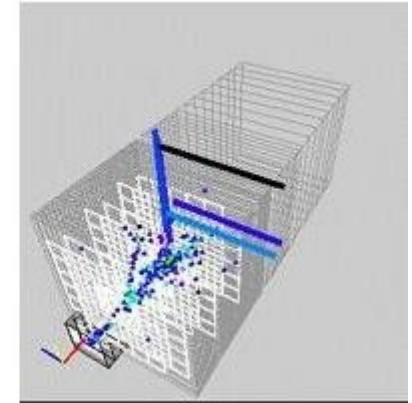
→ test and tune
of particle physics simulations (Geant4)

Detailed data from test beam makes
it possible to distinguish between
different physics simulations

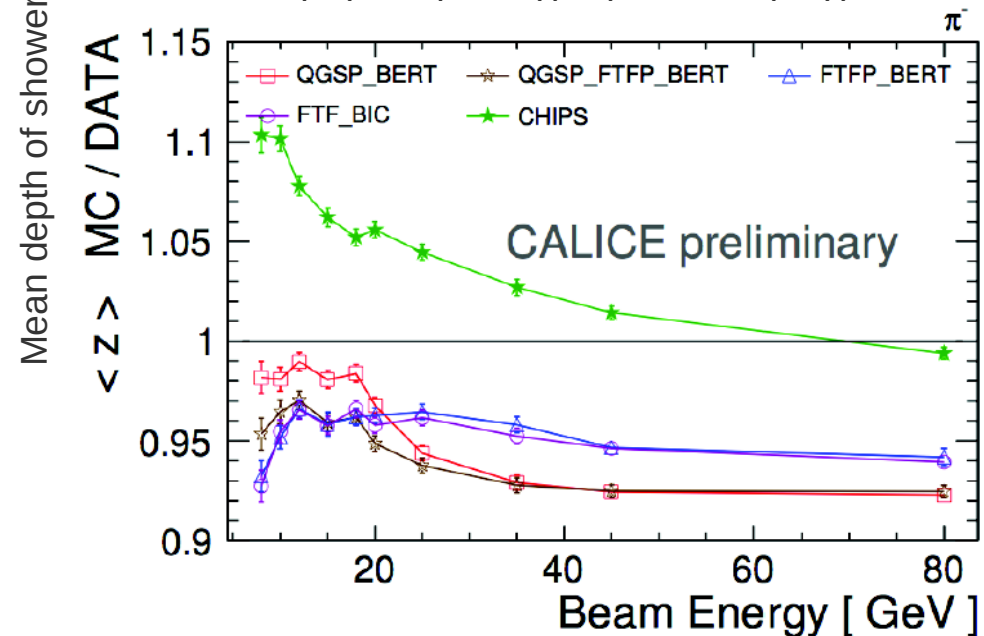
- longitudinal and lateral shower shapes
- shower starting point
- visible

Second generation technical prototype:

- adding time information
- timing of hadronic shower development

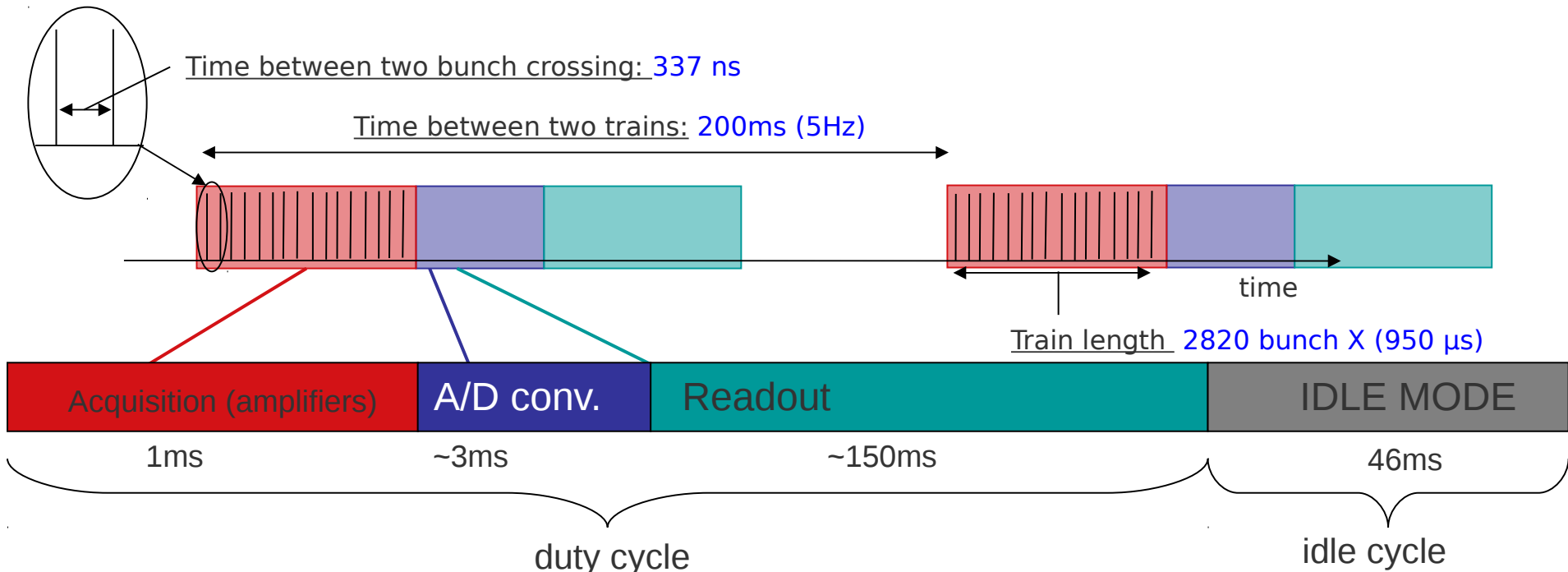


AHCAL physics prototype (Event display)



arXiv:1109.1982 [physics.ins-det]

ILC Bunch structure:



AHCAL ASICs are designed for ILC cycle:

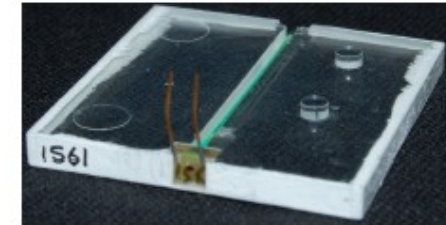
- measure timing only in a fraction of the duty cycle
- Analogue TDC measurement of voltage ramp in sync with ILC bunch train
- resolution of ~100ps in ILC mode
- **Power pulsing on the TDC**

Mayor modification for CERN:
provide zero time on a “fake” channel
with fast signal from upstream scintillator
and small time jitter on one chip

The setup has been optimized with an ITEP tile (delivered at DESY in November 2011)

ITEP Standard tile:

SiPM	Tile	border	surface	LY (Pixel)
CPTA	ITEP 1235	Acid polish	3M	$15,4 \pm 0,4$



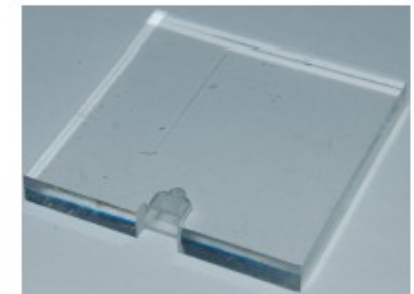
a first tile had been modeled from **Bicron BC-400** dimple for **direct coupling** to SiPM designed by MPI Munich coupled to **Hamamatsu MPPC S10362-11-50P**

} reference for comparison

a second tile had borders coated with aluminum evaporation

Reference tile+SiPM:

SiPM	Tile	border	surface	LY (Pixel)
MPPC	BC-400	air	3M	$10,4 \pm 0,4$
MPPC	BC-400	Al	3M	$9,3 \pm 0,4$



direct aluminum deposition shows less average LY value!

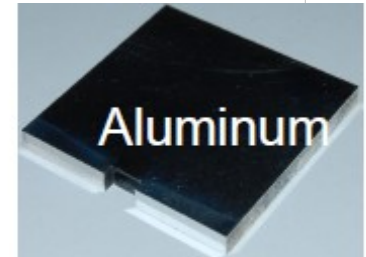


Several complete tile coatings have been studied:

Direct deposition on tile surface:

(from C.Soldner)

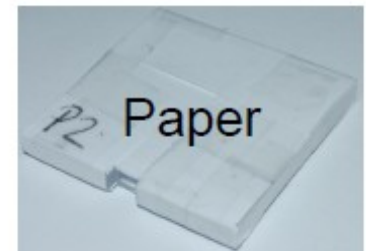
SiPM	tile	borders	surface	LY (Pixel)
MPPC	BC-400	Al	Al	~5
MPPC	BC-400	TiO ₂	TiO ₂	8,7



wrapping:

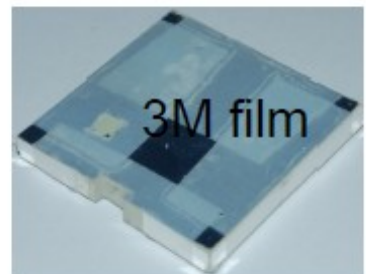
Reference:

SiPM	tile	borders	surface	LY (Pixel)
MPPC	BC-400	air	3M	10,4±0,4
MPPC	BC-400	3M	3M	28,8±0,4
MPPC	BC-400	paper	paper	19,7±0,4



With a different SiPM:

SiPM	tile	borders	surface	LY (Pixel)
Ketek II	BC-400	3M	3M	33,7±0,4



~15 % higher LY value due to better Ketek PDE

Wrapped tiles show promising results:

consistent with previous CALICE studies on coatings (e.g. Shinshu 2008 ScECAL studies, MPI Munich 2011 studies)



Next step: uniformity scan at MPI Munich

Though it has extensively demonstrated that tile non-uniformity have small impact on energy reconstruction of hadronic showers (see for example F.Simon talk on AHCAL meeting @ DESY, 13 December 2011)