



Automatic Detection of Single Event Upsets In the Read Out Chips of the CMS Pixel Detector

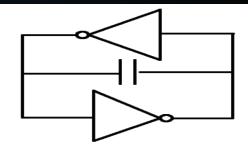
Luigi Calligaris (DESY)

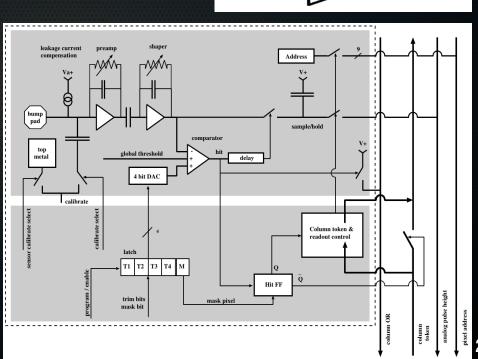


Effects of Single Event Upsets



- Pixel Detector: high rate of ionizing particles
- A bit can be flipped (SEU) by injected charge
- ROCs design protects them against SEUs
- Still, sometimes a SEU will happen in a critical bit register
 - Unexpected behaviour
 - Often the chip goes totally/partially silent
- Not possible to read ROC state (design choice)
 - Need for data-driven monitoring



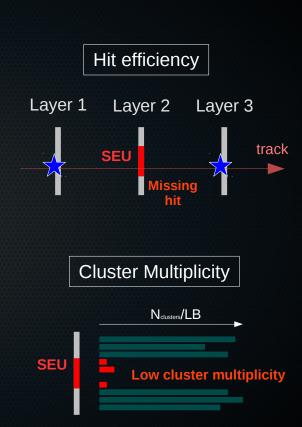






<u>Indicators for a silent ROC</u>

- Hit efficiency for tracks, multiplicity of digis (pixels above threshold), multiplicity of clusters
- Advantages of using clusters
 - Many clusters compared to tracks
 - Do not depend on tracking
 - Computationally cheap compared to tracks
- Aim: determine bad ROCs with a fine time granularity
 - Good data produced by them is not wasted
 - Useful to understand the phenomenon
- Observe: cluster occupancy for each ROC in each LumiBlock
 - Flag LumiBlocks where a ROC was bad

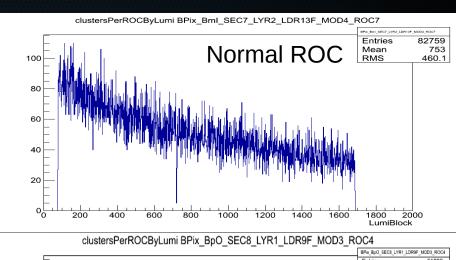


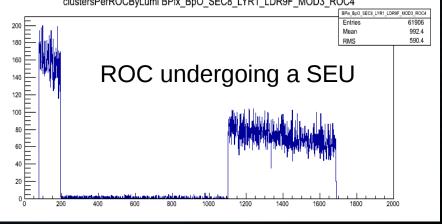




Bare cluster occupancy

- Count of clusters in that ROC for each LumiBlock (= 23 s)
- Values depend on instantaneous Luminosity
 - Bad for setting a fixed threshold
 - Time dependence
- Need to evolve to a variable that factorizes out
 - Instantaneous luminosity
 - Pile Up





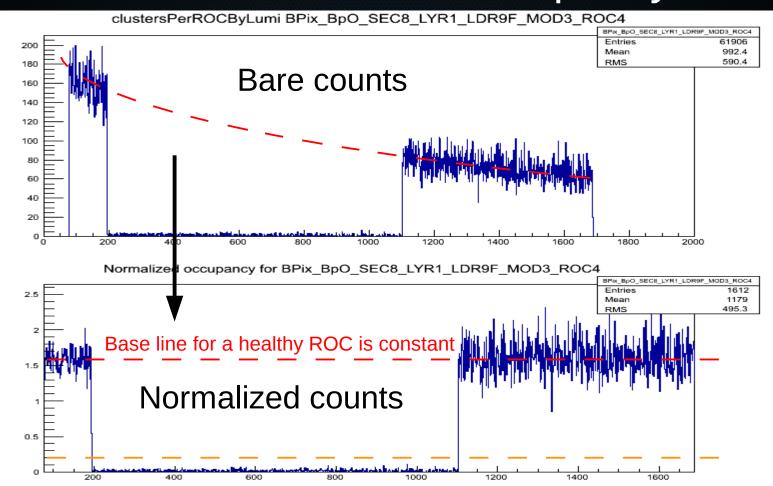




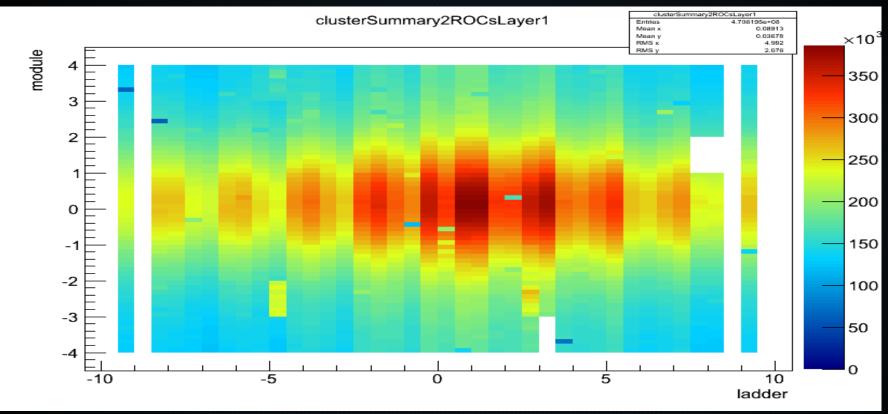
Normalized cluster occupancy

- Onormalized = (Nclusters in a ROC / Nclusters in all ROCs) * NROCs
- Factorizes out the luminosity dependence
- Differences in θ and layer expected
 - Acceptance changes
 - Particle flux changes
 - Beam spot not centered w.r.t. Barrel pixel
- Solution: calculate normalized occupancy in regions
 - Dividing detector along Z
 - Dividing detector in octants

Normalized cluster occupancy

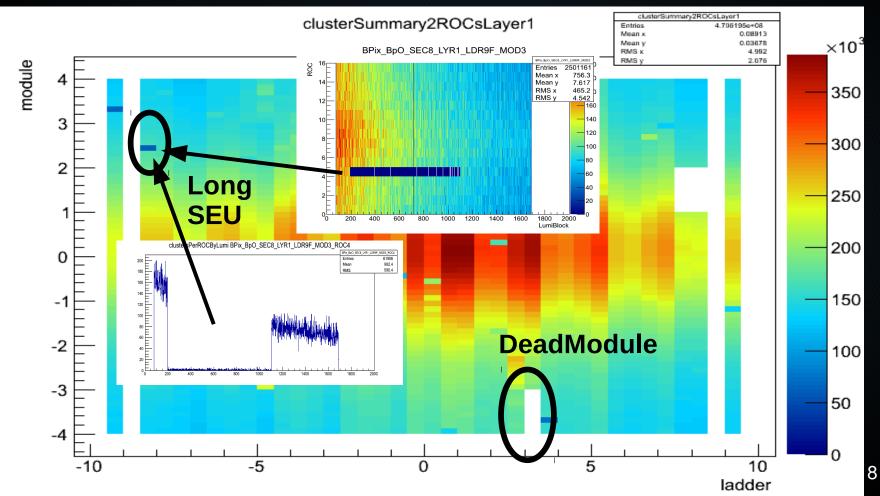


Bare occupancy map (LYR1) Run 191226

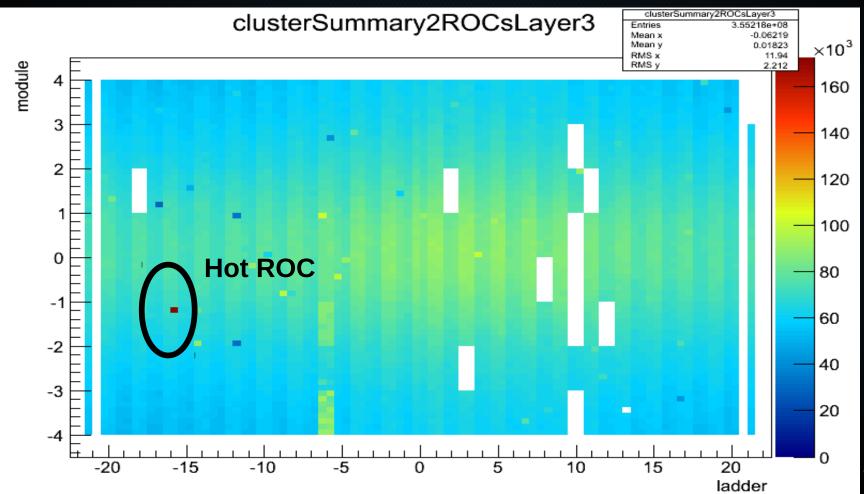


This summary map shows the cluster occupancy for each ROC, integrated over the whole run

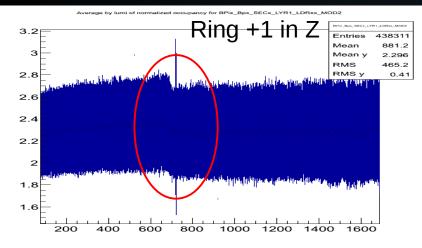
Bare occupancy map (LYR1) Run 191226

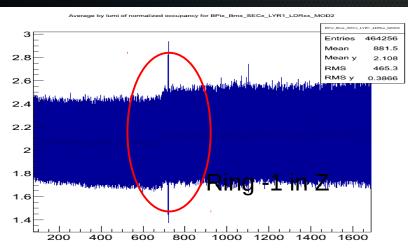


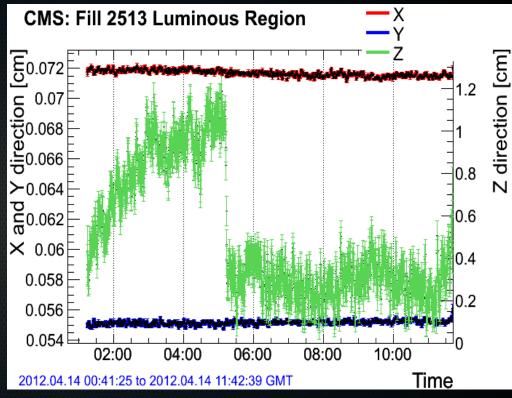
Bare occupancy map (LYR3) Run 191226



Cluster occupancy is quite sensitive...







Sees the beam spot Moving in z by 110 µm

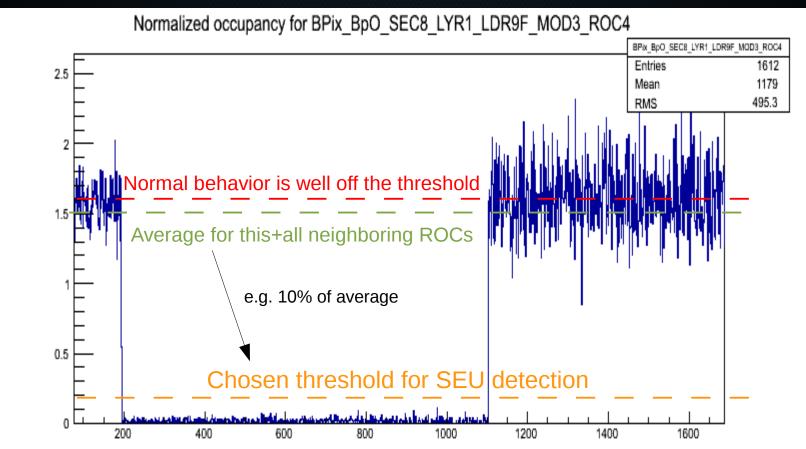




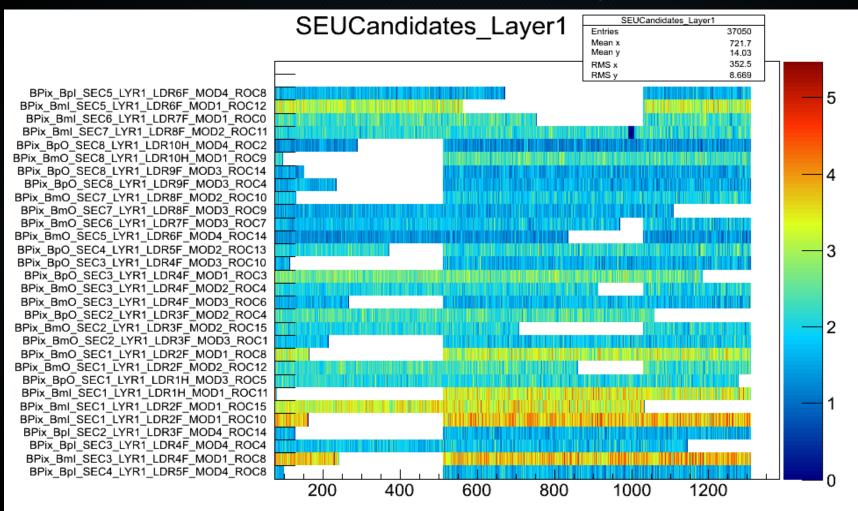
Algorithm to find SEU

- For each LumiBlock
 - Compute the average of the normalized occupancy
 - Choose a fraction of this value (e.g. 10%) as threshold
 - Compare with the normalized occupancy of each ROC
- If the ROC is flat zero, put it in the list of <u>dead ROCs</u>
- If the ROC stays under the threshold for more than N (e.g. 6) consecutive LumiBlocks
 - Put in the list of <u>SEU</u>
 - Plot its evolution as a function of LumiBlock

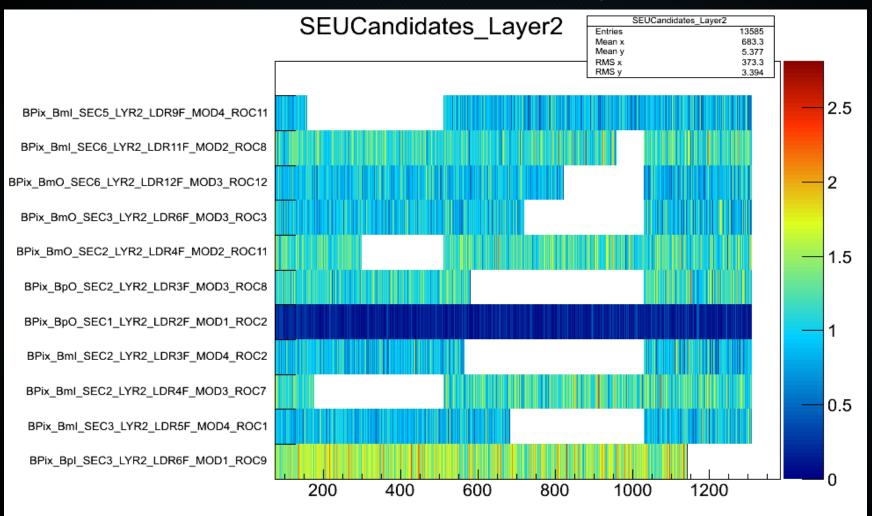
SEU seen from normalized occupancy



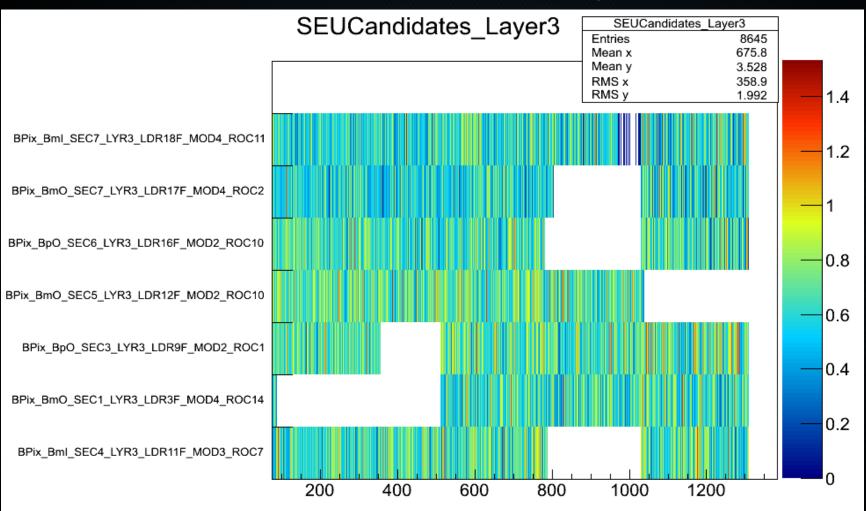
Run 201191 – Layer1



Run 201191 – Layer2



Run 201191 – Layer3



Summary & Outlook

- Tested a proof-of-concept setup to find SEUs
 - SEUs are identified by the algorithm
- Next:
 - Re-write the code as a bundle of CMSSW modules (ongoing), aimed at running on ExpressStream
- Goal:
 - Identify automatically SEUs in the Pixel detector
 - Provide a list of dead ROCs and modules, to ease their book-keeping

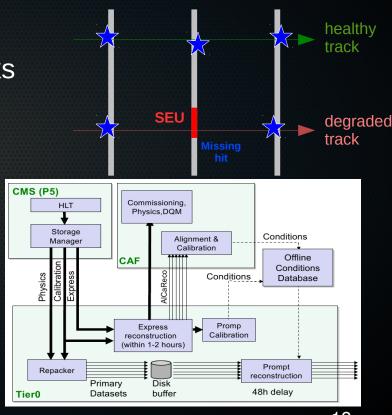
BACKUP





Tracking benefits from SEU detection

- Number of missing hits is a figure of merit for a track
 - A silent ROC pollutes good tracks with missing hits
- PromptReconstruction begins 48h after the end of run
- ExpressStream is available before for calibration, DQM, ...
- use this data to find silent ROCs, and upload to DB
 - PromptReco: improved tracking

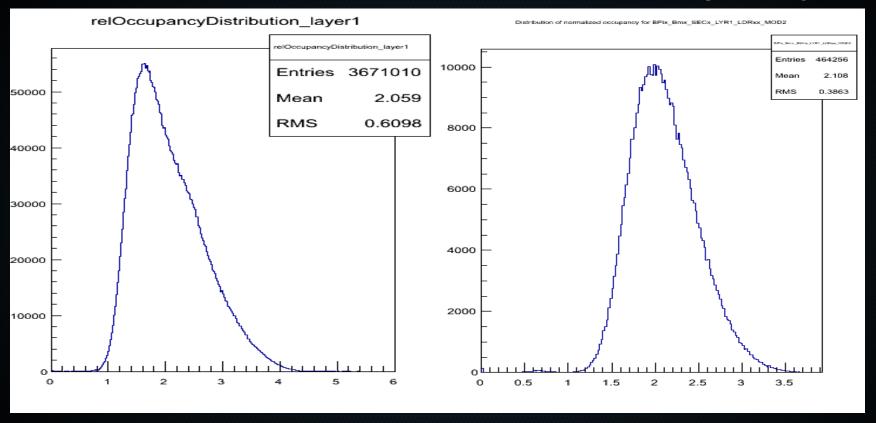


Laver 2

Laver 3

Layer 1

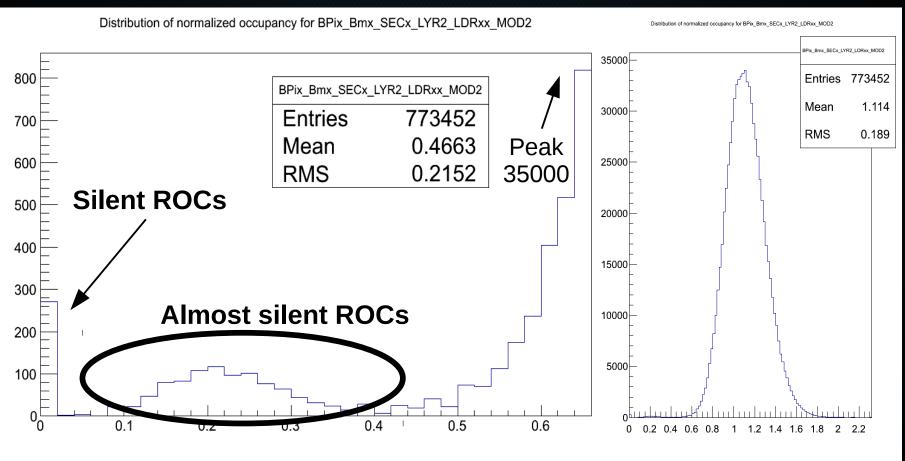
Distribution of normalized occupancy



All Layer 1 Integrated over run

Bmx_SECx_LYR1_LDRxx_MOD2 Integrated over run **Specific layer and ring in z**

Low tail of the distribution for a single ring



Implementation in CMSSW

Foreseen Data Objects

PixelROCId

- <u>detId</u> (uint32_t) + <u>roc number</u> (uint32_t)
- Identifies uniquely a ROC

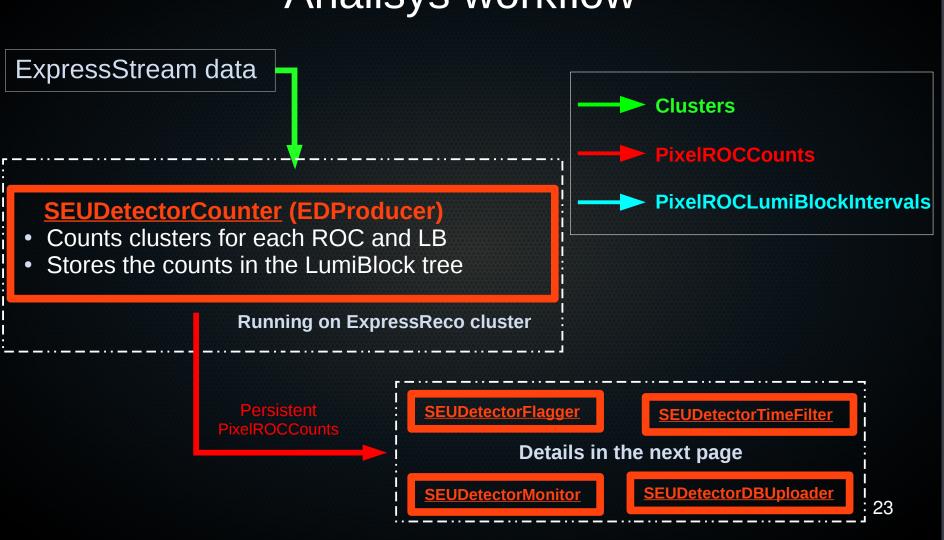
PixelROCCounts

- PixelROCId + counts (uint32_t)
- Transports the counts across CMSSW modules

PixelROCLumiBlockInterval

- PixelROCId + FirstSEULB + LastSEULB (both unsigned int)
- Describes the interval in which a SEU was in effect

Analisys workflow



Analisys workflow

SEUDetectorFlagger (EDProducer)

- Finds SEUs based on relative occupancy
- Produce
 PixelROCLumiBlockIntervals identifying ROC and duration





SEUDetectorTimeFilter (EDFilter ?)

 Accepts only SEU cand's lasting longer than N LBs (suppress fluctuations)

SEUDetectorMonitor (EDAnalyzer)

- Control plots in ROOT file
- Text file list of SEUs

<u>SEUDetectorDBUploader</u> (EDAnalyzer)

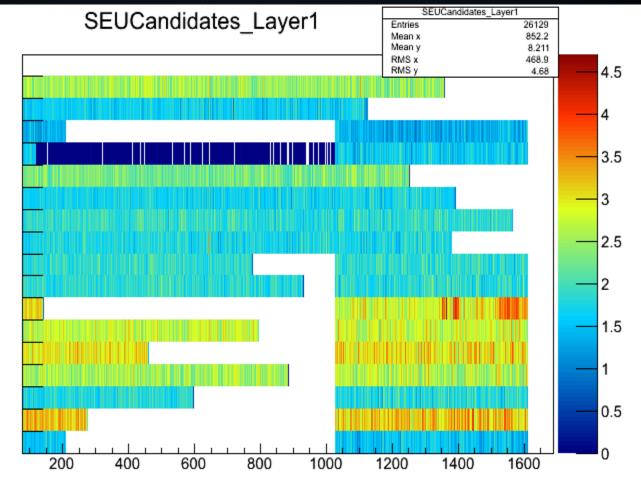
Uploads to DB using standard tools

Running on a single ExpressMerge machine

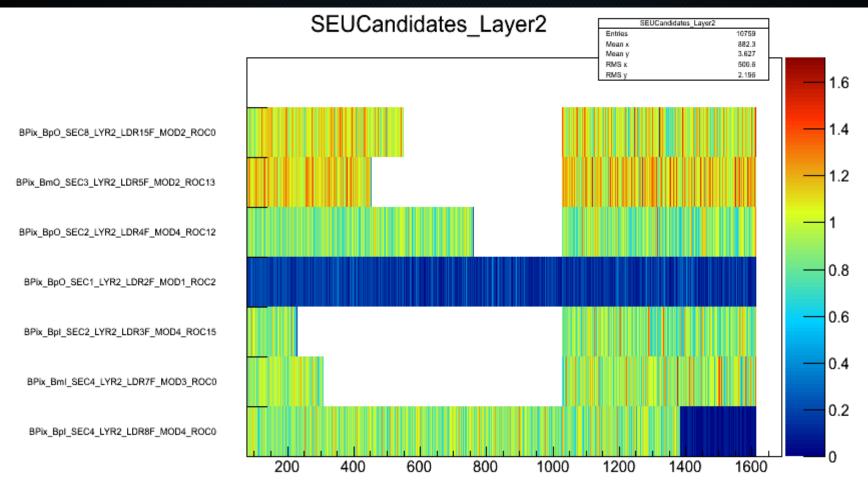
Some additional examples from different runs

Run191226 Layer1

BPix_Bpi_SEC5_LYR1_LDR6F_MOD2_ROC4 BPix_Bpi_SEC7_LYR1_LDR8F_MOD3_ROC0 BPix BpO SEC8 LYR1 LDR10H MOD4 ROC5 BPix_BpO_SEC8_LYR1_LDR9F_MOD3_ROC4 BPix_BmO_SEC7_LYR1_LDR8F_MOD1_ROC10 BPix_BpO_SEC5_LYR1_LDR6F_MOD3_ROC6 BPix_BpO_SEC4_LYR1_LDR5F_MOD3_ROC7 BPix_BpO_SEC2_LYR1_LDR3F_MOD4_ROC10 BPix_BpO_SEC2_LYR1_LDR3F_MOD3_ROC13 BPix_BpO_SEC1_LYR1_LDR2F_MOD3_ROC3 BPix BmO SEC1 LYR1 LDR2F MOD1 ROC4 BPix_Bml_SEC1_LYR1_LDR1H_MOD1_ROC15 BPix_Bml_SEC1_LYR1_LDR1H_MOD1_ROC12 BPix_Bml_SEC1_LYR1_LDR1H_MOD2_ROC9 BPix_Bml_SEC1_LYR1_LDR2F_MOD4_ROC0 BPix_Bpi_SEC2_LYR1_LDR3F_MOD1_ROC5 BPix_Bml_SEC4_LYR1_LDR5F_MOD4_ROC2

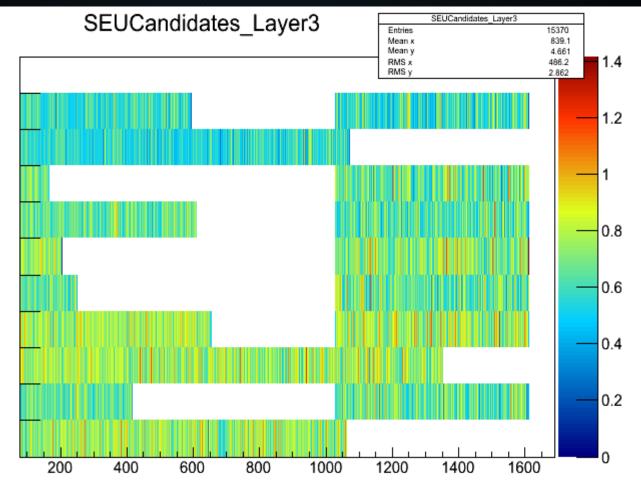


Run191226 Layer2



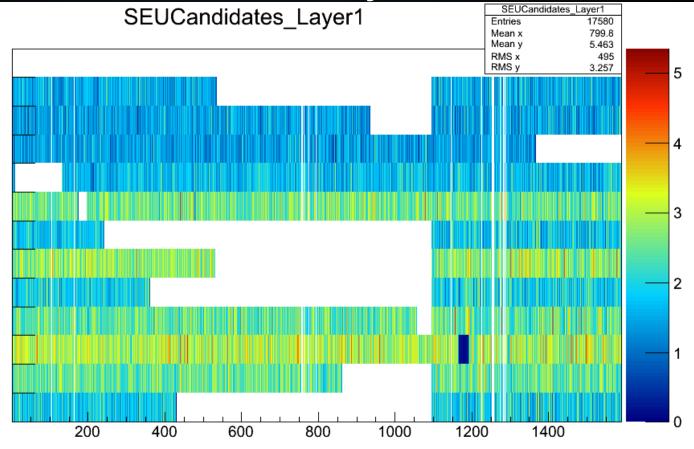
Run191226 Layer3

BPix Bml SEC7 LYR3 LDR18F MOD4 ROC6 BPix_BpI_SEC8_LYR3_LDR21F_MOD4_ROC10 BPix_BpO_SEC7_LYR3_LDR18F_MOD2_ROC9 BPix_BpO_SEC6_LYR3_LDR16F_MOD2_ROC12 BPix_BpO_SEC5_LYR3_LDR13F_MOD1_ROC15 BPix BmO SEC5 LYR3 LDR13F MOD2 ROC0 BPix_BpO_SEC4_LYR3_LDR11F_MOD1_ROC8 BPix_BmO_SEC3_LYR3_LDR9F_MOD1_ROC14 BPix_BpO_SEC3_LYR3_LDR7F_MOD3_ROC13 BPix BpO SEC1_LYR3_LDR2F_MOD2_ROC4



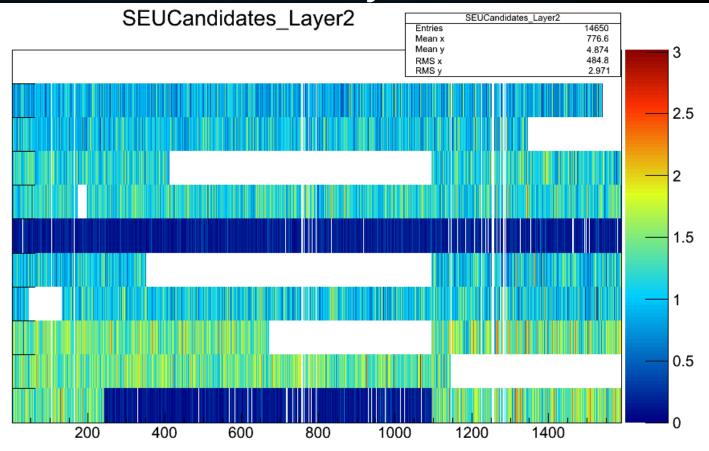
Run 195950 - Layer1

BPix_Bml_SEC7_LYR1_LDR8F_MOD4_ROC6 BPix_Bml_SEC7_LYR1_LDR8F_MOD4_ROC0 BPix_BpI_SEC8_LYR1_LDR9F_MOD3_ROC14 BPix_Bml_SEC8_LYR1_LDR10H_MOD3_ROC9 BPix_BpO_SEC6_LYR1_LDR7F_MOD1_ROC5 BPix_BpI_SEC1_LYR1_LDR1H_MOD4_ROC6 BPix_Bml_SEC1_LYR1_LDR2F_MOD2_ROC12 BPix_Bpl_SEC3_LYR1_LDR4F_MOD4_ROC7 BPix_BpI_SEC3_LYR1_LDR4F_MOD2_ROC13 BPix_BmI_SEC4_LYR1_LDR5F_MOD1_ROC5 BPix_BmI_SEC4_LYR1_LDR5F_MOD2_ROC6 BPix_BmI_SEC4_LYR1_LDR5F_MOD3_ROC4

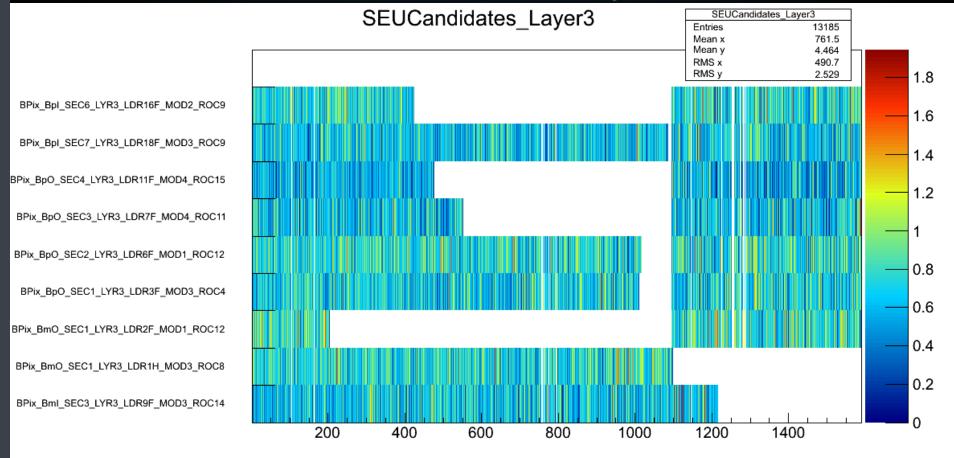


Run 195950 - Layer2



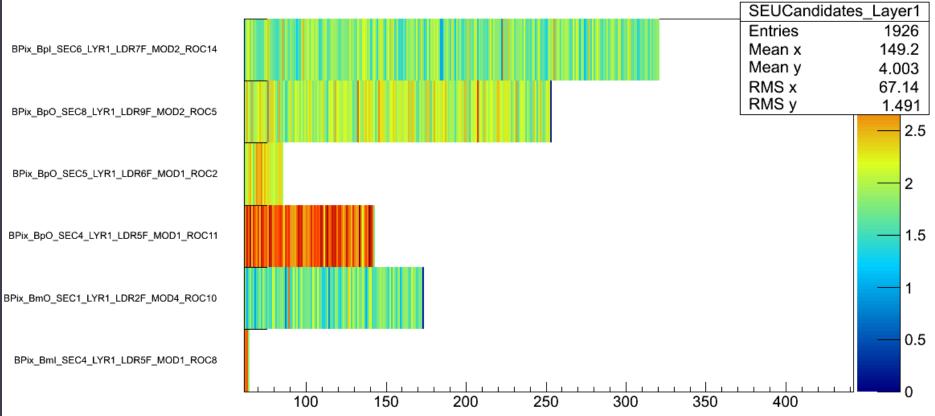


Run 195950 – Layer3



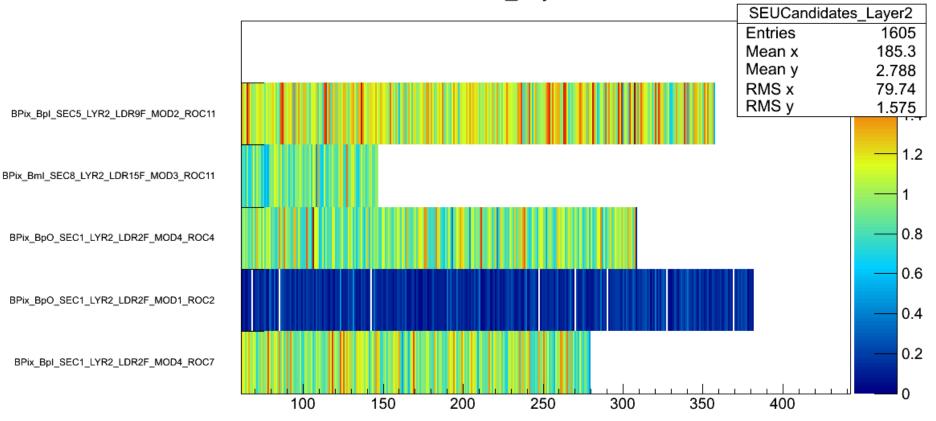
Run 196531 - Layer1





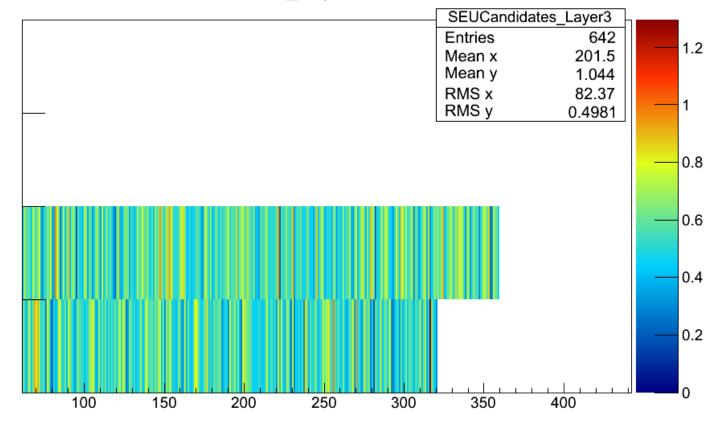
Run 196531 – Layer2

SEUCandidates_Layer2



Run 196531 – Layer3





BPix_Bml_SEC5_LYR3_LDR13F_MOD4_ROC1

BPix_BmO_SEC8_LYR3_LDR20F_MOD4_ROC6