

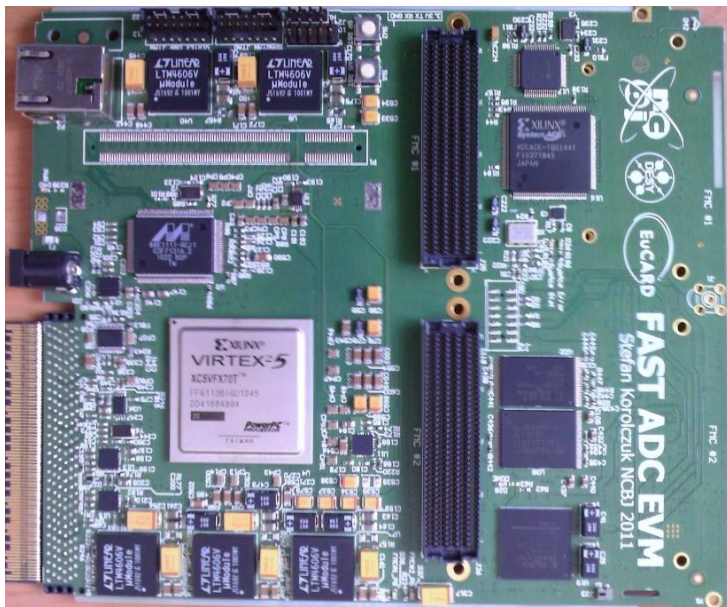
uFMC25 for BAM

Jaroslav Szewinski

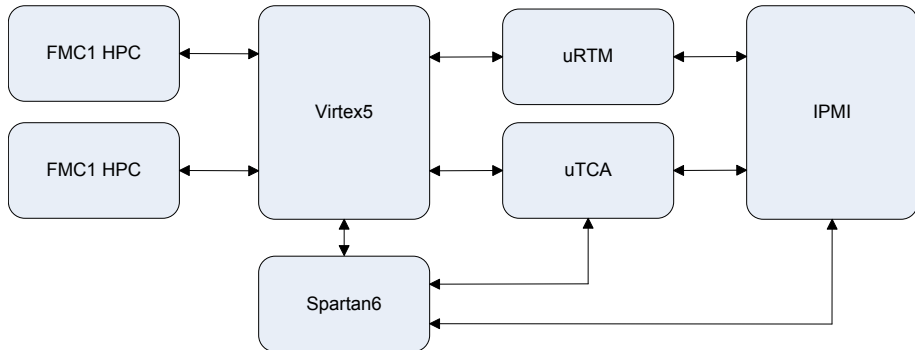
National Centre for Nuclear Research, Otwock-Świerk, Poland

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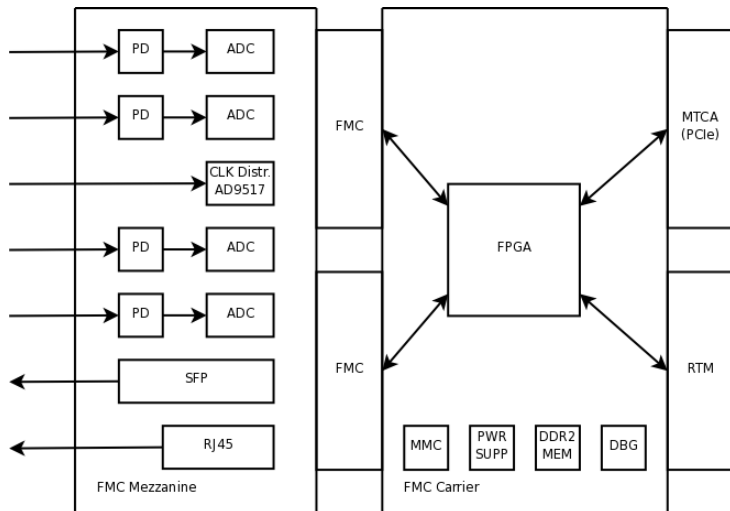




uFMC25v2 main block diagram



uTCA BAM - Carrier + Mezzanine



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- Easy to migrate to better (newer) FPGA without touching analog part

Thank You