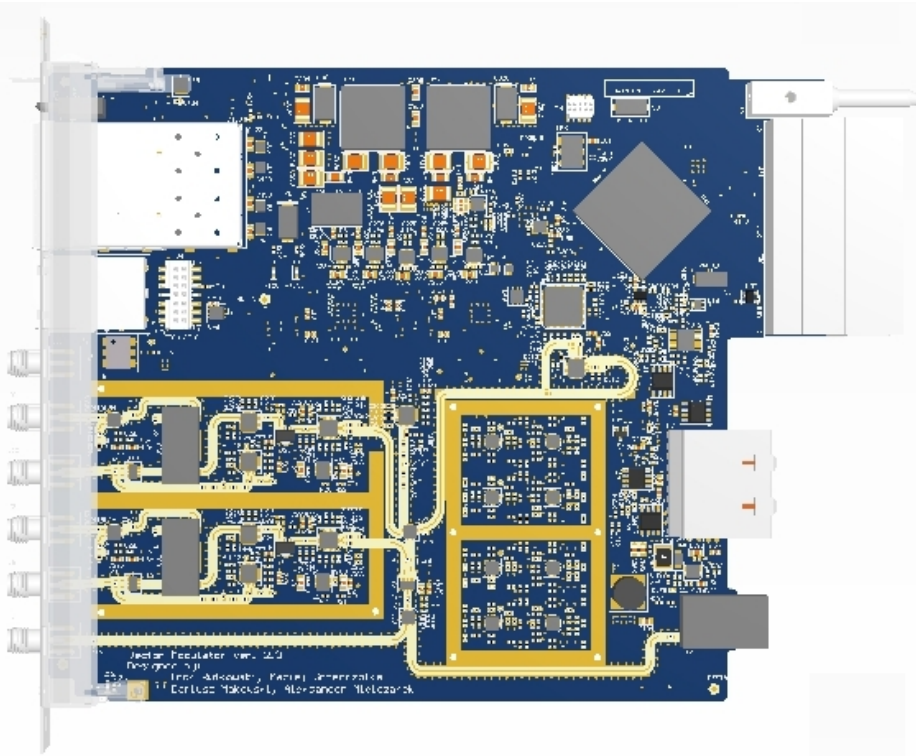


DRTM-VM02 Project Status



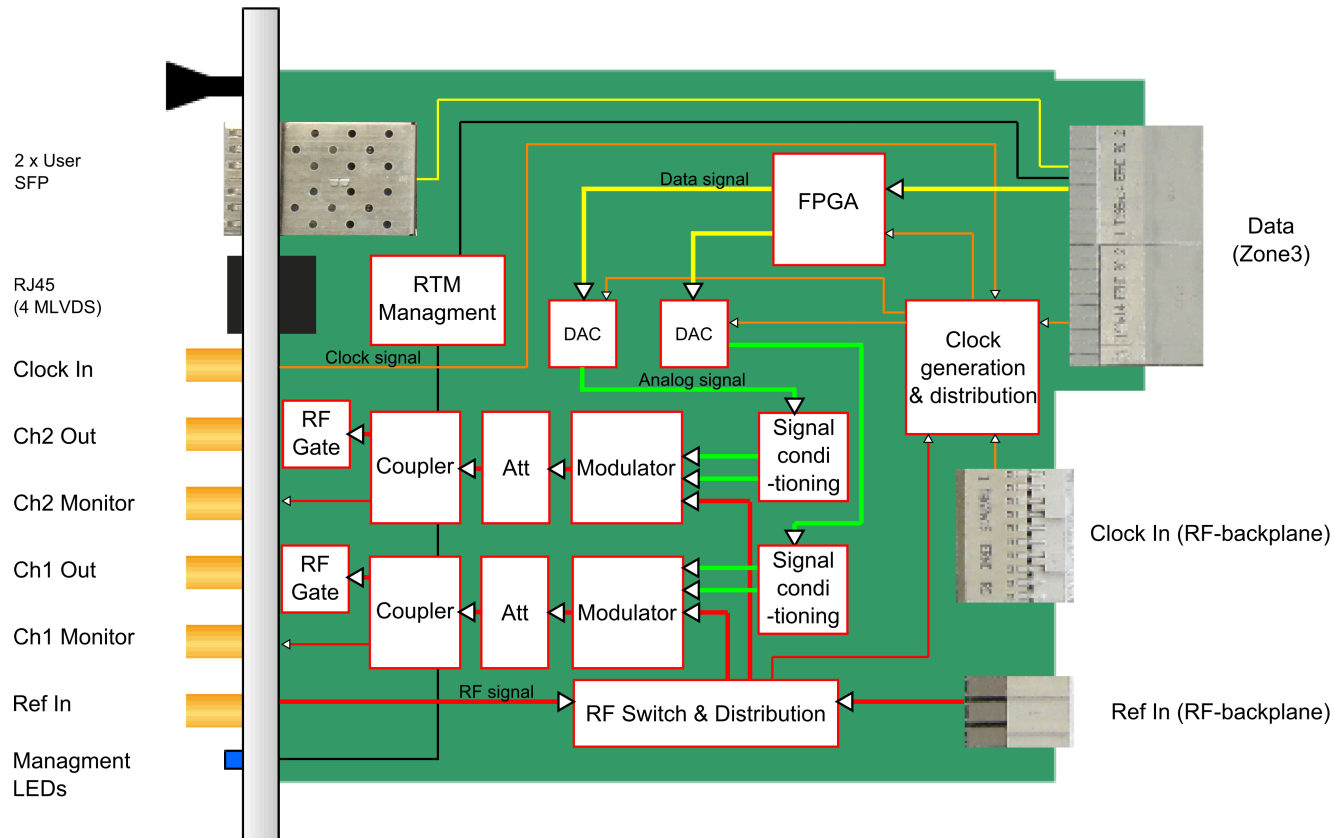
Igor Rutkowski

DRTM-VM02 Project Status
Advanced Techniques in LLRF
control for XFEL – Collaboration
Workshop, Otwock - Świerk
20.02.2013

Agenda

- > Board overview
- > Revision 2.0 changes
- > Status
- > Plans

Board overview



Revision 2.0 changes

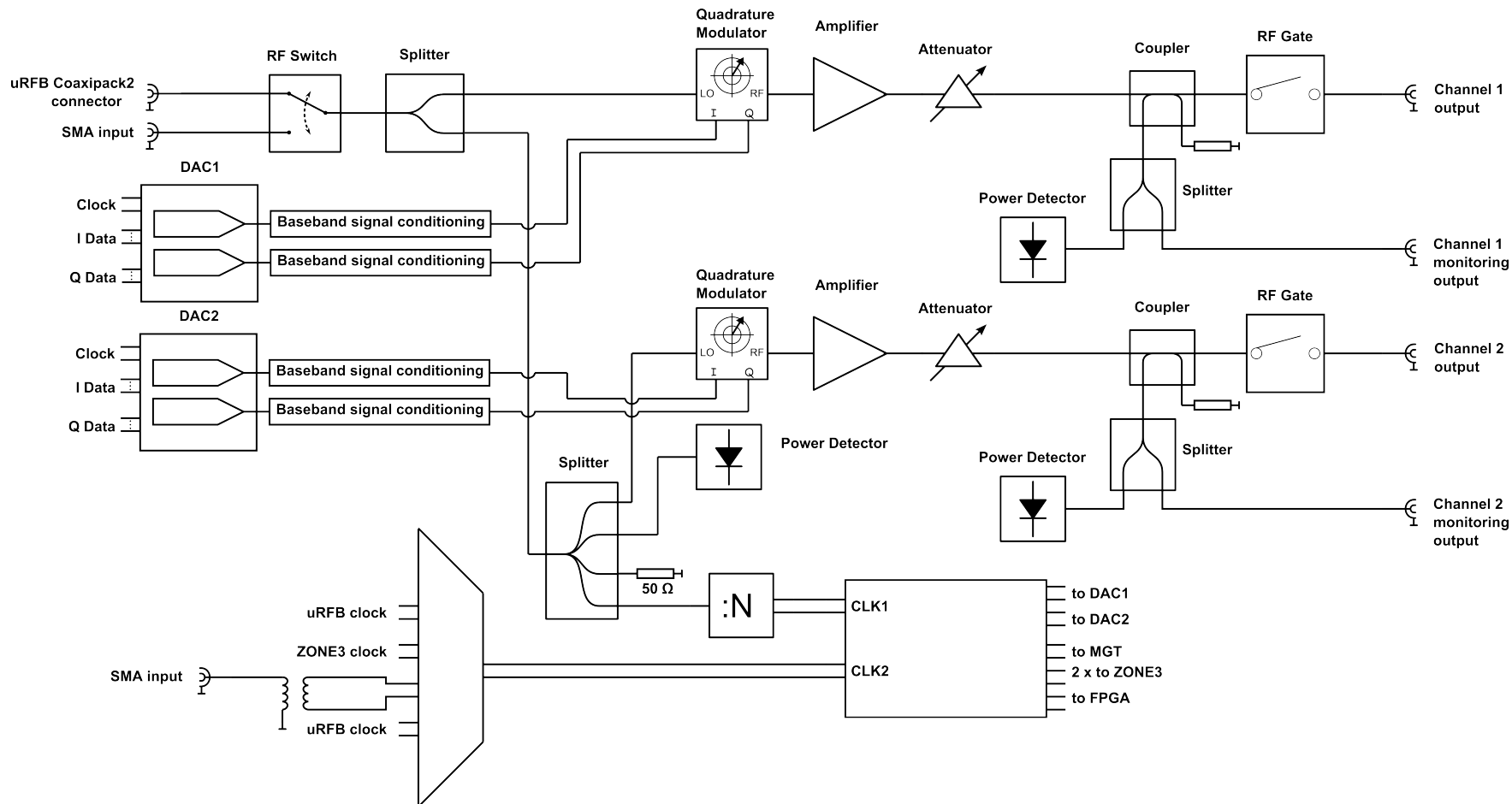
> Specification changes

- operation frequency range: 100 MHz – 6 GHz
- working clock input
- Zone3 classification
- new module management
- monitoring outputs
- new DACs
- new RF backplane PS pinout
- RF backplane clock

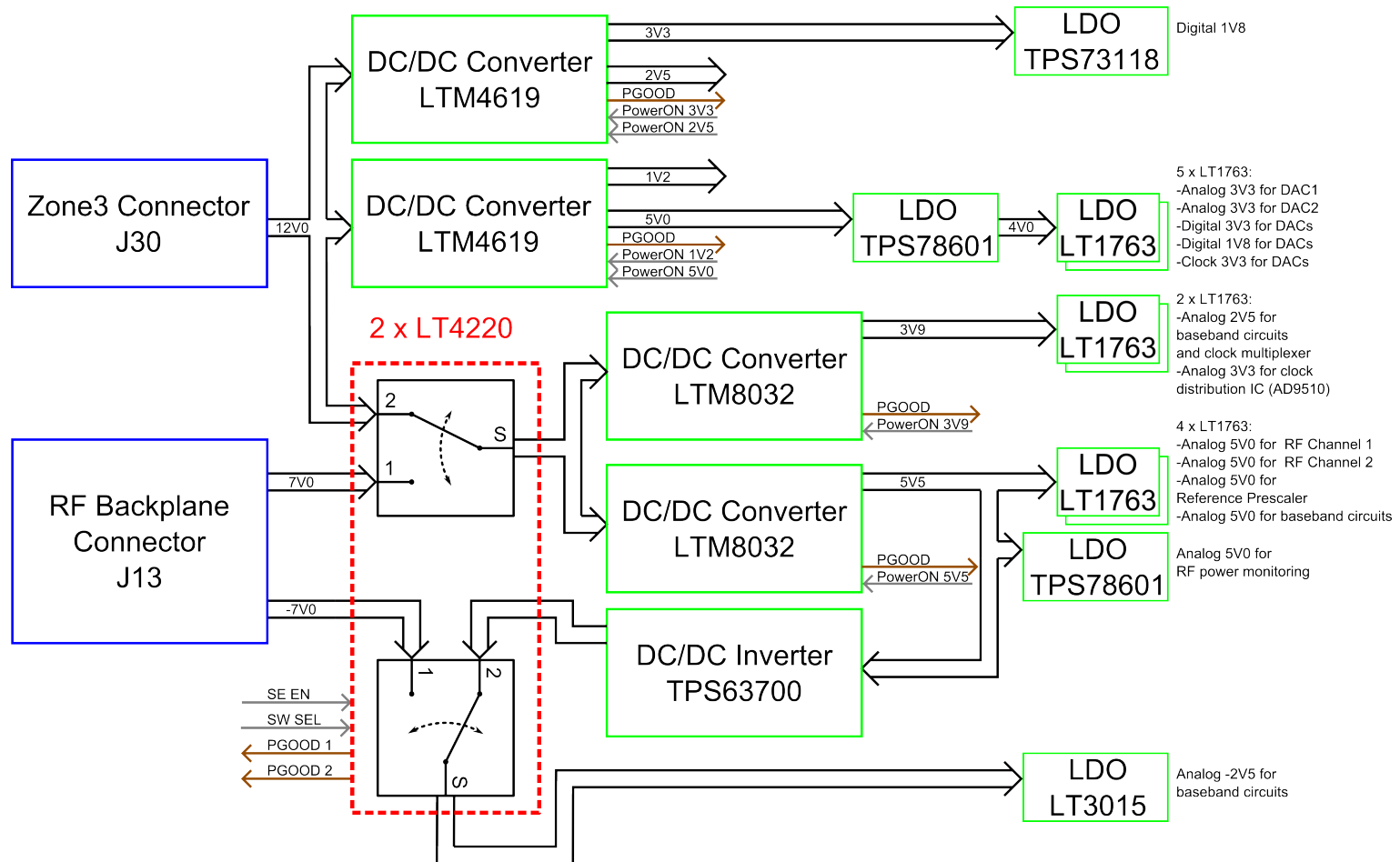
> Preparation for mass production / licensing

- replace jumpers with electronic switches

RF and clock distribution



Power Supply System



> Last minute changes

- ECAD (Mentor → Altium)
- MMC
- Zone3
- Interlocks
- RF backplane pinout

> PCB finally done

> Waiting for quotation from ILFA

> BOM prepared & corrected for life time management

> Last order to be placed

Plans

- > PCB manufactured: late March
- > Boards delivered to DESY: early / mid April
- > Functional tests and performance characterization: end of April
- > Test stand (Maciek Grzegorzówka): end of April
- > Firmware (board layer, MG): end of April
- > User Manual: mid May
- > Gain and offset calibration?
- > Front panel
- > IPAC 2013?
- > FRU?

Related projects / presentations

- > DRTM-DWC8VM1 (MH)
- > Low frequency VM status (MG)

Questions



Thank You for attention!