

# BAM-FMC

Samer Bou Habib



# Agenda

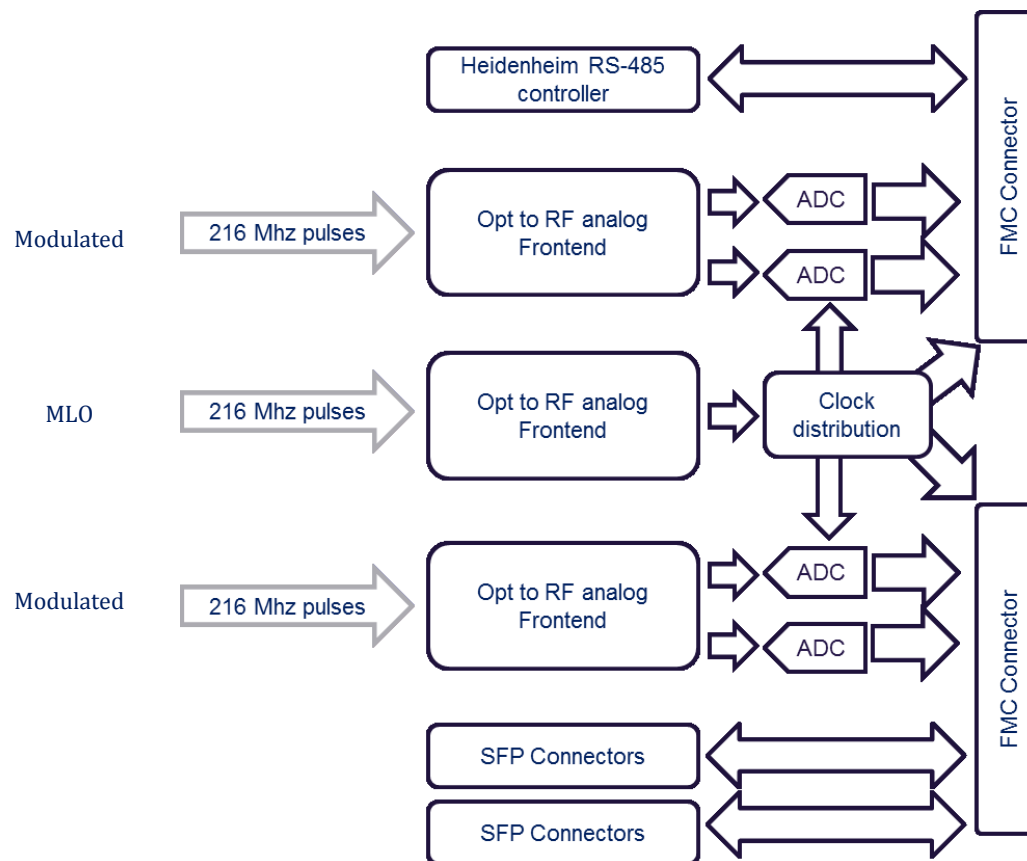
- Introduction
- Main Issues
- Status and Timeline

# Introduction\*

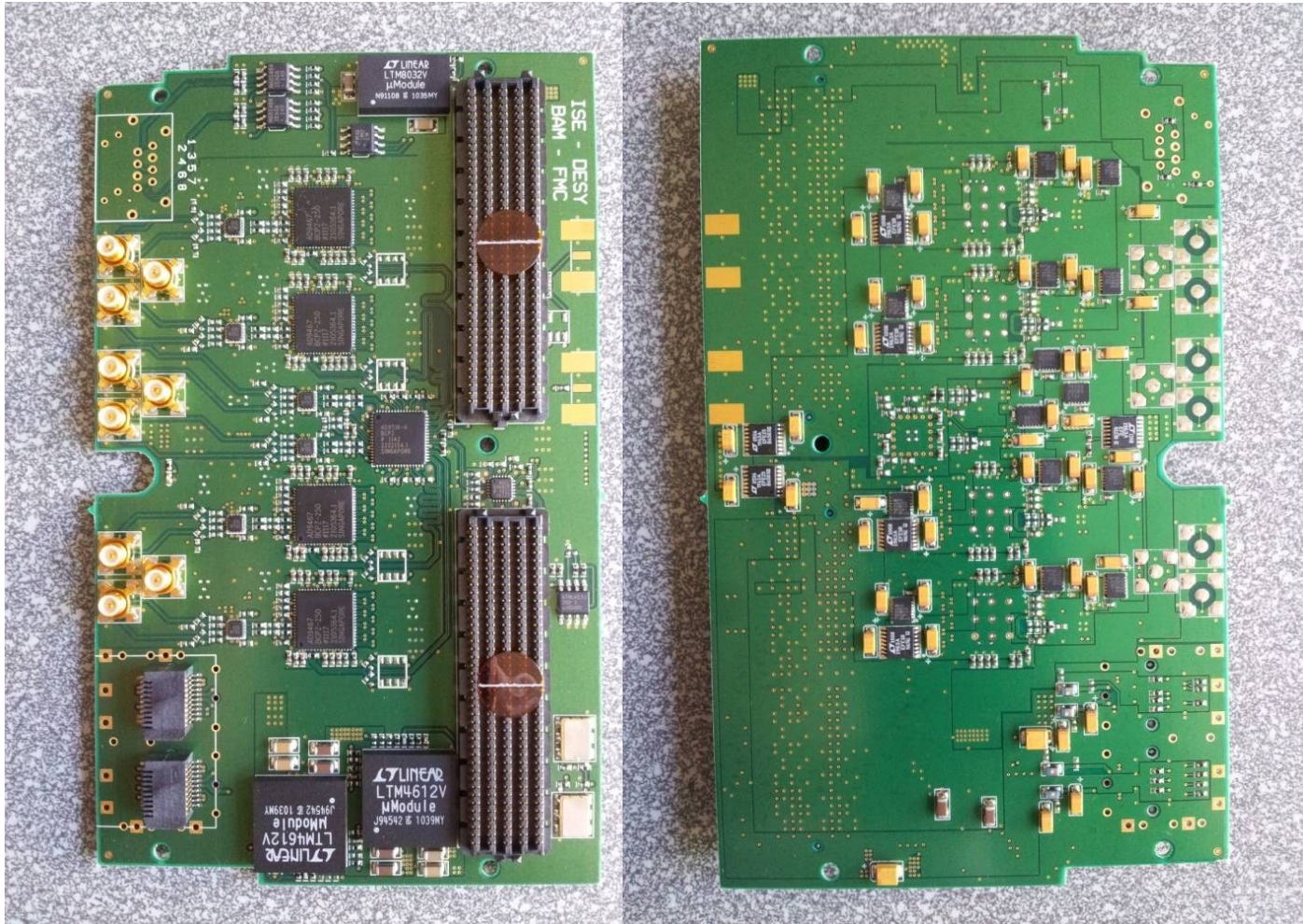
- FMC standard
- Double width
- Mainly prepared to be used with DAMC-FMC25
- 216 MSPS (no pulse skipping)

[\\*Details](#)

# Block Diagram



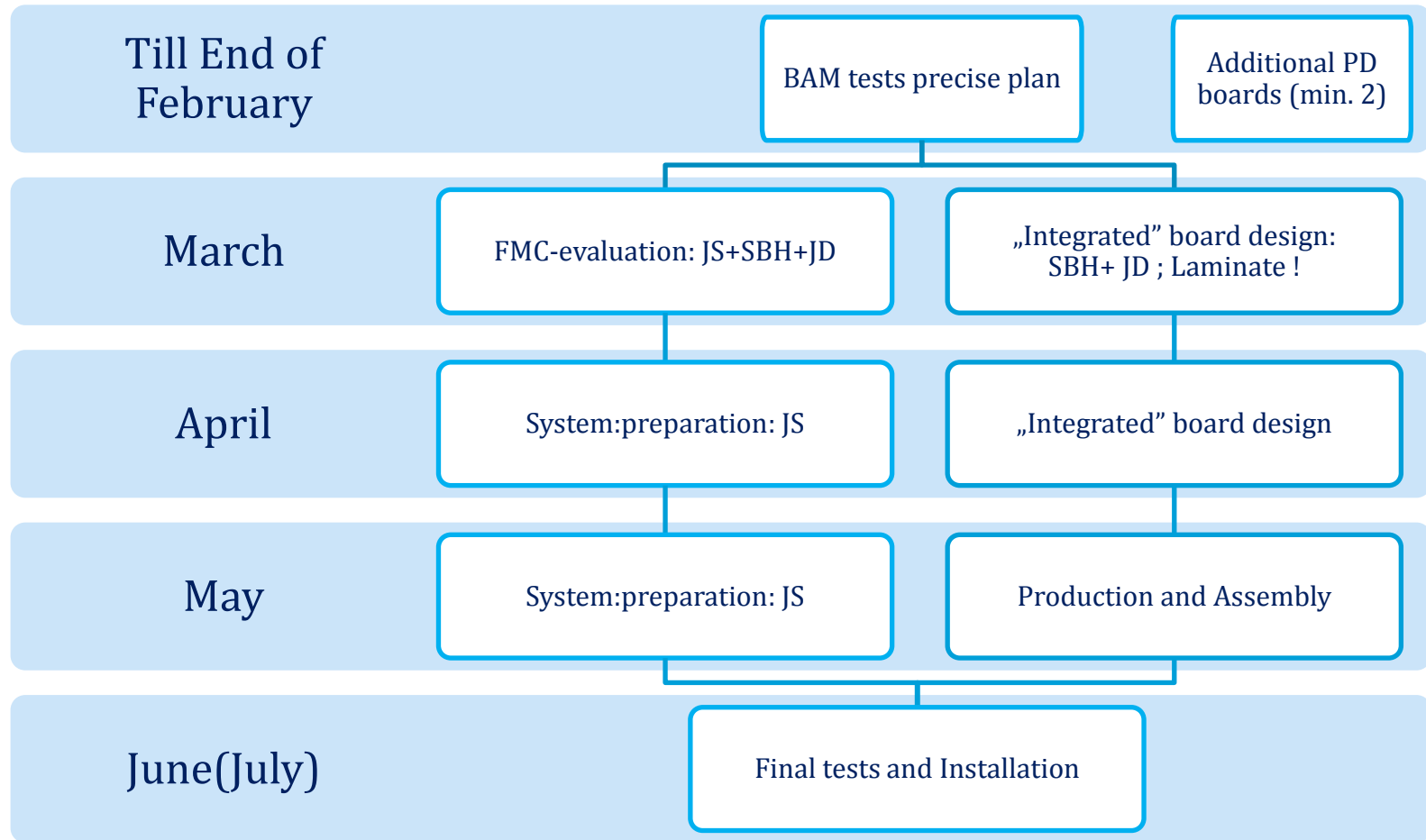
# BAM-FMC



# Main Issues – what took so long...

- Optical-to-RF circuits not fit for new PD  $\Rightarrow$  "old" will be used
- PD performance very delicate and dependant on used laminate
- Lack of manpower! – partially resolved

# Timeline



# What is to be done (fast)

- 2-3 PD boards assembly and tests: JD
- Laminate for integrated FMC
- CLK circuits evaluation: JS + SBH
- ADC circuits evaluation: JS + SBH
- Integration of FMC with PD
  - Power levels: SBH + JD
  - CLK/Time shifting : JS + SBH



# Thank you for your attention

[S.BouHabib@stud.elka.pw.edu.pl](mailto:S.BouHabib@stud.elka.pw.edu.pl)