Contribution ID: 52 Type: not specified

## **BAM Discussion**

Wednesday 20 February 2013 15:40 (50 minutes)

Requirements for the complete BAM-system:

- \* The lowest level: availability of electronics for control of motors, encoder, analogue I/O, etc.
- \* The hardware level: cabling, signal lines, limitations in latency and bandwidth etc., interfaces to other subsystems (LLRF, BPM, ...)
- $^{\star}$  The software level: firmware for communication between cards in the uTCA crate, server for the BAM readout and control
- \* Fix all specifications
- \* Prepare schedule for developments and tests

Session Classification: Optical synchronization