2nd MTCA Workshop for Industry and Research

Contribution ID: 37

## **MTCA.4 Front End Processing**

Wednesday 11 December 2013 12:30 (8 minutes)

The best that the technology can provide in MTCA.4 Signal Processing

## Summary

Last Digital Processing Technologies availlable:

- FPGAs by far the highest ratio processing power/consumption for parallel computing
- Virtex-7 example
- FPGAs high speed transceivers
- ADCs
- Vita 57 used for high speed coders
- IC-FEP-TCAa Design in partnership with DESY :
- IC-FEP-TCAa/IC-ADC-FMCc presentation
- Reference Designs

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Session Classification: New products

Track Classification: New products