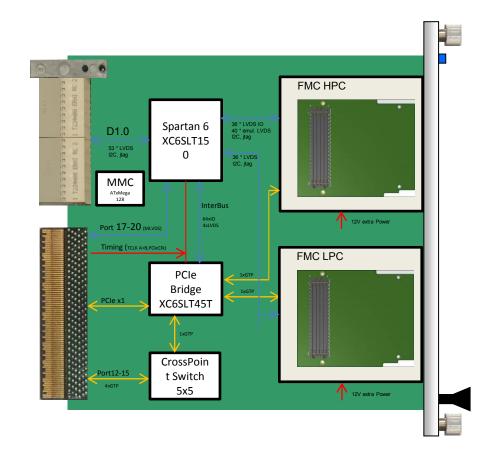
AMC Dual FMC Carrier Board.

DAMC-FMC20

Features

- •Advanced-Mezzanine Card (AMC), Class D1.0 compatible
- Supports 2 FMCs
- •ANSI/VITA 57.1 (LPC and HPC) compliant
- Double width MTCA.4
- •Extra power connector for FMCs
- •Onboard Spartan-6 FPGA
- •IPMI 1.1 compliant MMC
- RoHS compliant

The DAMC-FMC20 is a general purpose FPGA Mezzanine Card (FMC) carrier board compliant to the MTCA.4 standard. The carrier simultaneously supports a low pin count (LPC) and a high pin count (HPC) FMC module compliant to the ANSI/VITA 57.1 standard. The selected FPGA topology uses two Spartan-6 devices to achieve high-performance at moderate costs. It supports one Gigabit Link for each FMC module and in addition an extra 12V power connector for high current FMC applications. It provides one PCI link (AMC.1 type 1 compliant). The carrier is software-reconfigurable over PCI express and via MMC. The AMC ports 12-15 are connected via cross point switch to a FPGA Gigabit Link. The carrier management is compliant to the latest recommendation MMC V1.0. The carrier Zone 3 is compliant to the Class D1.0



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Status Produktion and Firmware 25.6.2013

DAMC-FMC20

Hardware

Design readyLayout ready

PCB ready (2 parts)

Assembly intended 15.May, delayed now 28.Juni (ZE)

Test, Debug
4 Weeks (31 Juli)

Firmware

• MMC start with software from the DMC2 as base version (with minor change ,90% done)

later use of standard MMC advanced design

PCIe
Xilinx template PCI exp 1 lane ep PCIe bridge (installed not tested)

Application Part in preparation

Documentation

Datasheet Version 1.0
Specification Version 1.1
Manual in preparation
Test spezifikation in preparation