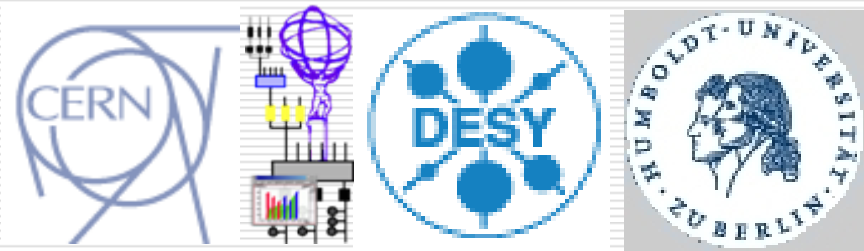
TriP in M6

Judita Mamuzic

DESY

11.03.2008



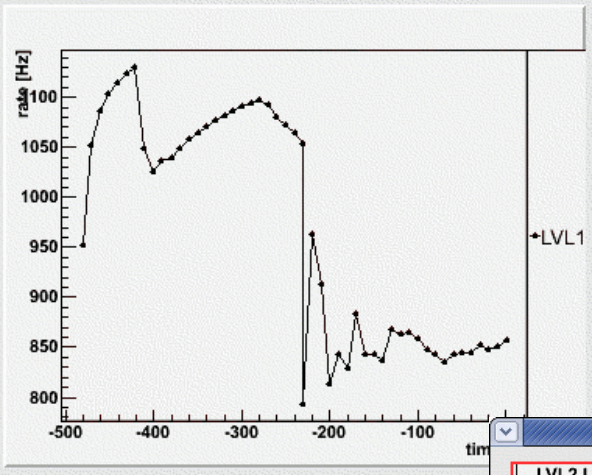
Overview

- M6 usage
- Farm tests
- Tdaq-01-09-00
  - To-do
  - Discussions
- Conclusion

Trigger Presenter

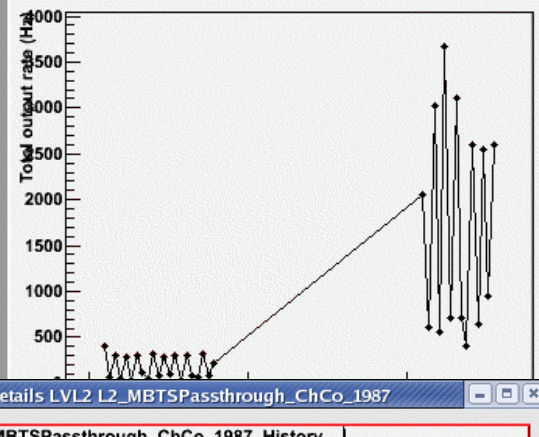
File Help

LVL1 rate (Hz) 0%



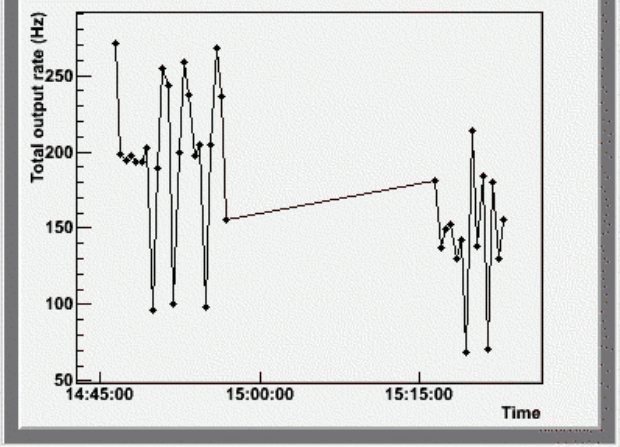
LVL2 rate (Hz) 74%

LVL2 History



EF rate (Hz) 70%

EF History



EF FARMS (N/A)

SF09 SF10 SF11 SF12 SF13 SF06 SF07

str_HLT_Cosmics_MU2_physics
94.07

str_HLT_Cosmics_J8_physics
0.00

EF_CaloDummy_ChCo_1906
0.00

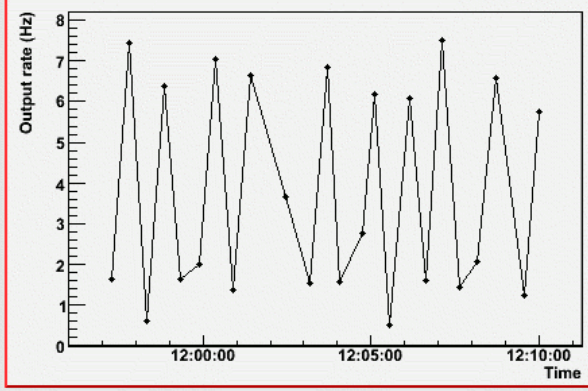
EF_Dummy_ChCo_1902
94.07

EF_Mufast_ChCo_1941
25.93

EF_MBTSPassthrough_ChCo_1987
4.73

Details LVL2 L2_MBTSPassthrough_ChCo_1987

LVL2 L2_MBTSPassthrough_ChCo_1987 History

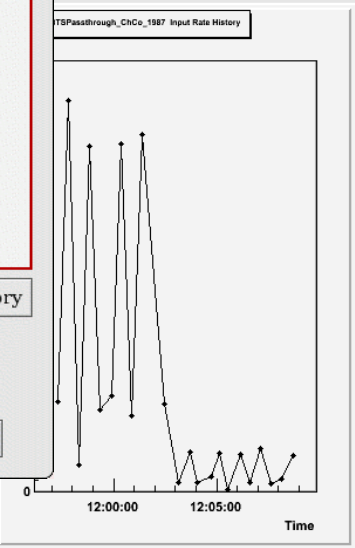


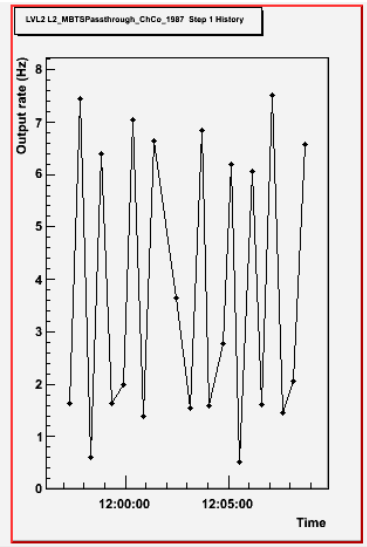
Show Step History

	Step Descripti	Acceptance
1	Input Rate	63.645
2	Step 1	1.258
3	Prescale	1.258
4	Forced Accept	1.258

Close

Steps of LVL2 L2_MBTSPassthrough_ChCo_1987

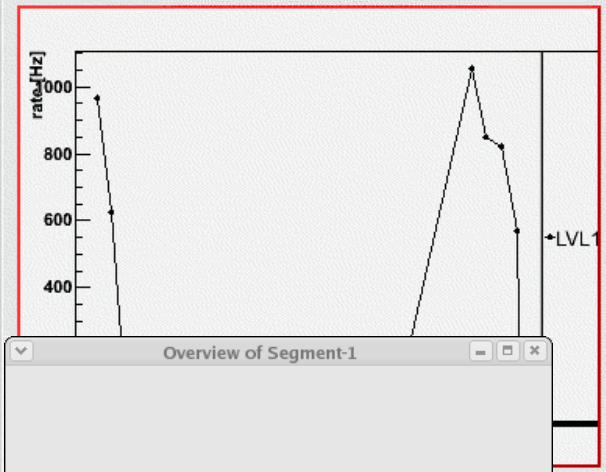




Update now Auto-Update Close

File Help

L1 rate (Hz) 0%



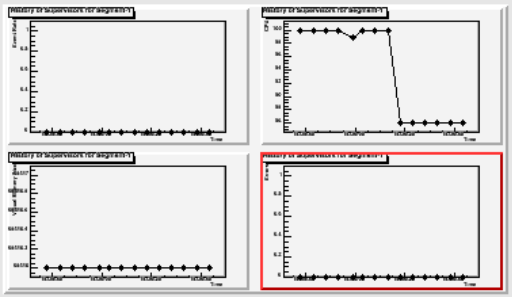
L2 rate (Hz

Overview of Segment-1-1

L2PU 8162 Rate:0.00 Errors: 0 CPU: 0 Memo:816788	L2PU 8163 Rate:0.00 Errors: 0 CPU: 0 Memo:804244	L2PU 8164 Rate:0.00 Errors: 0 CPU: 0 Memo:790084	L2PU 8165 Rate:0.00 Errors: 0 CPU: 0 Memo:817472	L2PU 8166 Rate:0.00 Errors: 0 CPU: 0 Memo:842648	L2PU 8167 Rate:0.00 Errors: 0 CPU: 0 Memo:839916
L2PU 8168 Rate:0.00 Errors: 0 CPU: 0 Memo:810600	L2PU 8169 Rate:0.00 Errors: 0 CPU: 0 Memo:789744	L2PU 8170 Rate:0.00 Errors: 0 CPU: 0 Memo:802816	L2PU 8171 Rate:0.00 Errors: 0 CPU: 0 Memo:818548	L2PU 8172 Rate:0.00 Errors: 0 CPU: 0 Memo:805884	L2PU 8173 Rate:0.00 Errors: 0 CPU: 0 Memo:800640
L2PU 8174 Rate:0.00 Errors: 0 CPU: 0 Memo:860924	L2PU 8175 Rate:0.00 Errors: 0 CPU: 0 Memo:845944	L2PU 8176 Rate:0.00 Errors: 0 CPU: 0 Memo:816764	L2PU 8177 Rate:0.00 Errors: 0 CPU: 0 Memo:863216	L2PU 8178 Rate:0.00 Errors: 0 CPU: 0 Memo:818780	L2PU 8179 Rate:0.00 Errors: 0 CPU: 0 Memo:809912
L2PU 8180 Rate:0.00 Errors: 0 CPU: 124 Memo:1079440	L2PU 8181 Rate:0.00 Errors: 0 CPU: 0 Memo:924860	L2PU 8182 Rate:0.00 Errors: 0 CPU: 0 Memo:847224	L2PU 8183 Rate:0.00 Errors: 0 CPU: 1 Memo:840344	L2PU 8184 Rate:0.00 Errors: 0 CPU: 0 Memo:852968	L2PU 8185 Rate:0.00 Errors: 0 CPU: 0 Memo:825604
L2PU 8186 Rate:0.00 Errors: 0 CPU: 0 Memo:868408	L2PU 8187 Rate:0.00 Errors: 0 CPU: 0 Memo:798288	L2PU 8188 Rate:0.00 Errors: 0 CPU: 0 Memo:860928	L2PU 8189 Rate:0.00 Errors: CPU: Memo:88	L2PU 8190 Rate:0.00	L2PU 8191 Rate:0.00

Overview of Segment-1

Segment-1-1
Rate: 0.00
Errors: 0
CPU: 125
Memo:25204932

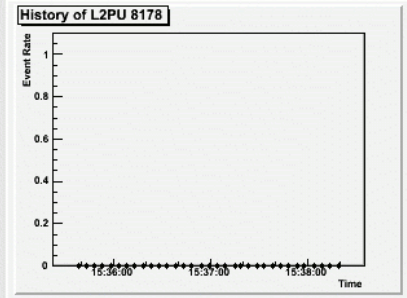


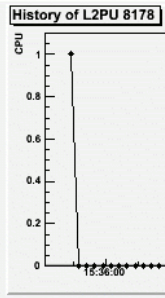
Close

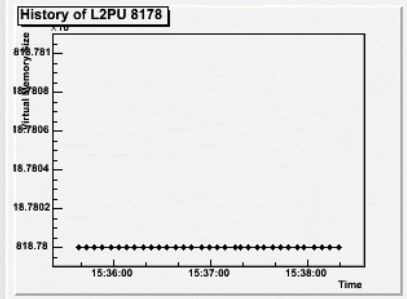
L2 History

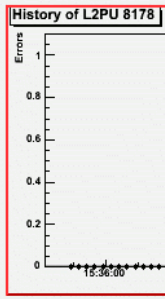
Segment-1 Rate:0.00 Errors:000 CPU:100 Memo:68516	Segment-2 Rate:0.00 Errors:000 CPU:100 Memo:66076
Segment-3 Rate:0.00 Errors:000 CPU:100 Memo:69676	Segment-4 Rate:0.00 Errors:000 CPU:099 Memo:69392

Overview of L2PU 8178









Update now ☑ Auto-Update

Farm Tests:

- Iterating over servers, looking for L2, L2SV and EF
- Subscribing to these servers
- Reading out the object names of correct types (PROC, L2PU, L2SV, PT)
- Reading out the n, m and ID from object name
- Structure and history
- GUI

Tdaq-01-09-00

- Qt-Root collisions – make green

(will be added to the release tomorrow)

- EF, finalizing

TODO (or as a patch)

- Global info

Discussions

- Adding hostname into RPOC type
- L2PU dead case (look into .xml)

Conclusion:

- Committed to tdaq-01-09-00

- Previous functionalities in use
- Farm status for L2 tested – works as expected
- Finalizing