

5th Belle II PXD/SVD Workshop and 15th International Workshop on DEPFET Detectors and Applications

Thursday 23 January 2014

SVD Parallel Session - Sem 5 (14:00 - 18:45)

| time | [id] title | presenter |
|-------|--|----------------------|
| 14:00 | [29] Indroduction | IRMLER, Christian |
| 14:10 | [30] L3 preparation status and plans | BARONCELLI, Tommaso |
| 14:30 | [31] L4 preparation status and plans | KAMESH |
| 14:50 | [32] L5 preparation status and plans | IRMLER, Christian |
| 15:10 | [33] L6 preparation status and plans | ONUKE, Yoshiyuki |
| 15:30 | [34] FW/BW assembly status and plans | BETTARINI, Stefano |
| 15:50 | Coffee break | |
| 16:20 | [35] Ladder parts production status | HARA, Koji |
| 16:45 | [36] Sensor testing results | BOSISIO, Luciano |
| 17:10 | [37] SVD mechanics status and plans | BUCHSTEINER, Florian |
| 17:30 | [39] FADC and APVDAQ system status and plans | FRIEDL, Markus |
| 17:50 | [38] FADC and FADC_controller firmware | NAKAMURA, Katsuro |
| 18:30 | [44] Contingency for discussion | |

Friday 24 January 2014

SVD Parallel Session - Sem 5 (09:00 - 10:50)

| time | [id] title | presenter |
|-------|--|-------------------|
| 09:00 | [54] FTB - changes in hardware and firmware. Recent status | OSTROWICZ, Wacław |
| 09:20 | [41] SVD software status | BOZEK, Andrzej |
| 09:40 | [42] Raw-SVD unpacking | PAOLONI, Eugenio |
| 10:00 | [43] SVD Schedule | IRMLER, Christian |
| 10:20 | [40] QA/QC discussion | HARA, Koji |