

DS800 status, results and production plans

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Agenda

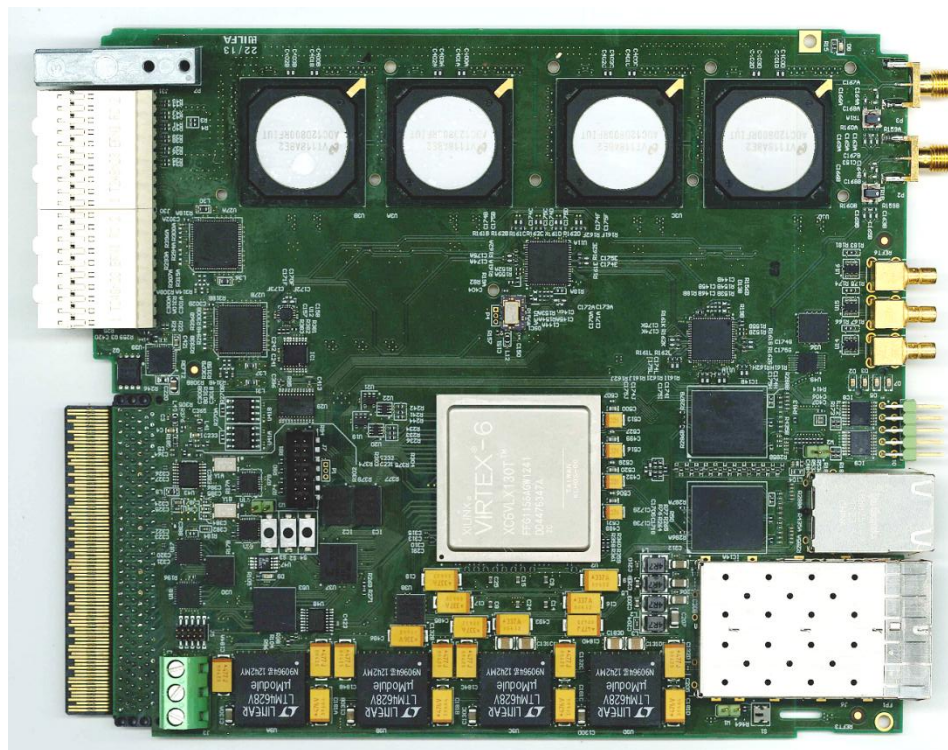
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- Status
- Results
- Plans

Status

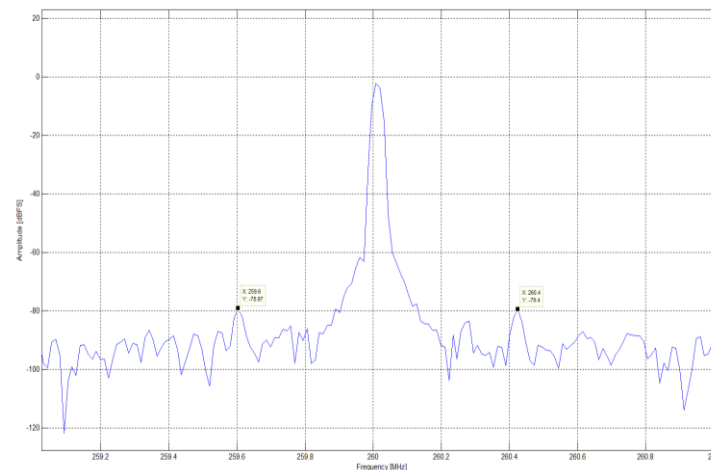
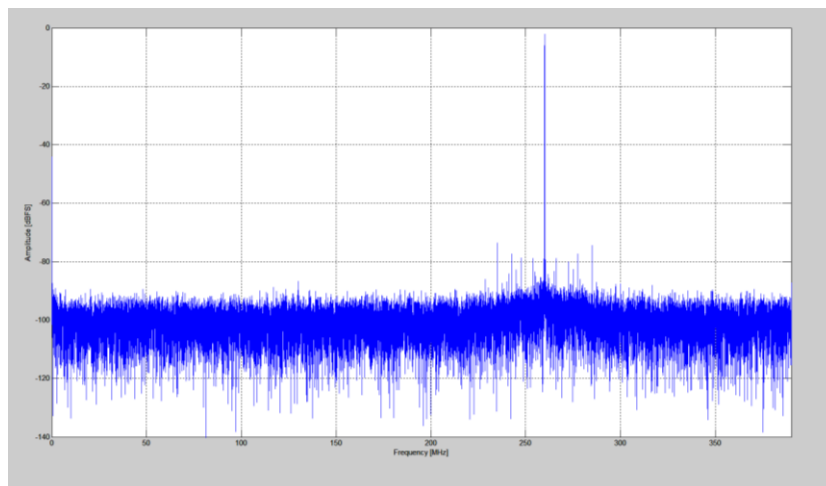
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- All ICs tested and operational (except SRAM)
- ADC performance checked
- Mechanical issue
- Rev 1.1 in progress



Results @ 780 MSPS

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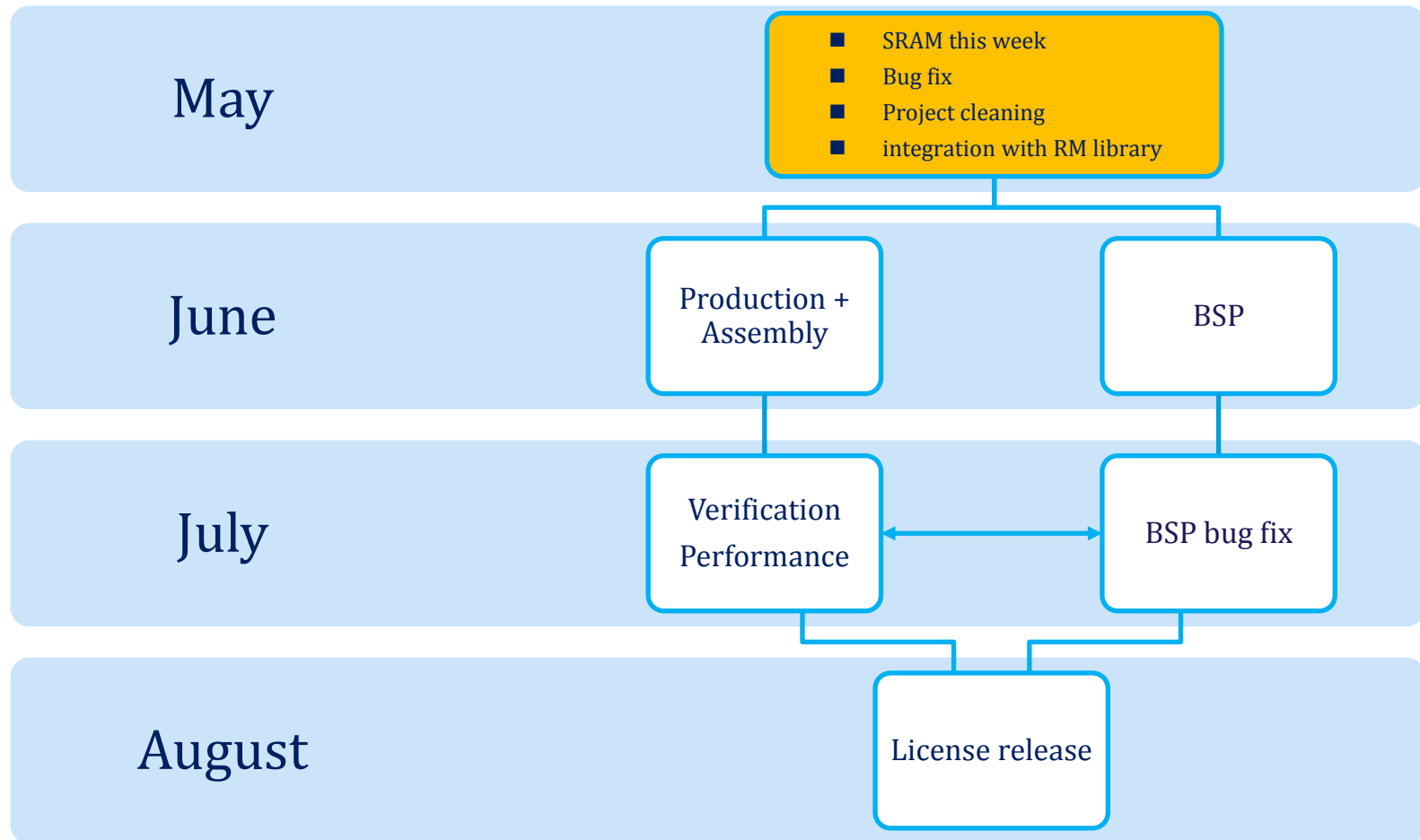


Sampling Parameters	SNR (SINAD) [dB]	PSD [dBfs/Hz]	ENOB [bits]	SNR (SINAD) in 1 MHz BW [dB]	ENOB in 1 MHz BW [bits]
Measured	53.4	140	8.57	79.3	12.88
Data sheet Value for 1.5GHz input	55.1	149	8.8	No data	No data

2/3 demodulation scheme	260 MHz BW (3 samples)	1 MHz BW	100 KHz BW
RMS relative amplitude resolution	0.00086	0.00012	5.54 e-005
RMS Phase resolution [°]	0.09	0.016	0.0055

Plans

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Thank you for your attention

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