

Test-Stand for High-Performance FPGA Computing Module

Thursday, December 11, 2014 5:30 PM (0:15)

Abstract content

The European-XFEL project requires a powerful computing module for the Low-Level RF system. The processing power will be provided by CM045 module delivered by Vadatech – a fruit of successful commercialization of DAMC_TCK7 module developed for DESY by DMCS. About 100 boards will be ordered and will have to be carefully evaluated before installation in the accelerator tunnel. Manual testing of every important component on each board would be enormous task. In order to boost the effectivity and reduce risk of missing any important problem a semi-automated Test-Stand was proposed. The test-stand suite is composed of two FPGA firmwares and a set of Python scripts. The test suite verifies operation of the power supplies, FPGA, CPLD, MMC, all the memories and fast data links. Finally a PDF report is generated for each tested module. The presentation will provide more details on how the solution is implemented and how it parts are interfacing together.

Summary

Primary author(s) : Mr. MIELCZAREK, Aleksander (Lodz University of Technology)

Co-author(s) : Dr. MAKOWSKI, Dariusz (Lodz University of Technology)

Presenter(s) : Mr. MIELCZAREK, Aleksander (Lodz University of Technology)

Session Classification : Applications in research facilities

Track Classification : Applications in research facilities