

3rd MicroTCA Workshop for Industry and Research

Monday 08 December 2014 - Thursday 11 December 2014

DESY
Programme

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Wednesday 10 December 2014

Applications in research facilities - FLASH II Hall (16:00-18:00)

- Conveners: Mr. Dirksen, Vollrath

time	title	presenter
16:00	Status of MicroTCA at DESY and XFEL	REHLICH, Kay
16:15	uTCA and MTCA.4 hardware used at KEK	MICHIZONO, Shinichiro
16:30	A MicroTCA Power Module Test Pad and a short status report on the MTCA evaluation project at CERN	MENDEZ, Julian
16:45	SLAC National Accelerator Laboratory MicroTCA.4 Collaboration	YOUNG, A
17:00	Open Hardware MTCA development at Creotech Instruments SA and WUT	KASPROWICZ, Grzegorz Kasprowicz
17:15	MicroTCA.4-based Timing System used at XFEL and FLASH/FLASH2	STECHMANN, Christoph
17:30	MicroTCA.4 Event Receiver for MRF Timing System	PIETARINEN, Jukka
17:45	An MTCA White Rabbit Timing Receiver for FAIR	MEHLE, Marko

Thursday 11 December 2014

Applications in research facilities - FLASH II Hall (14:15-14:30)

- Conveners: Larsen, Raymond

time	title	presenter
14:15	Conceptual Design of LCLS II BPM System on MicroTCA.4	XU, Chengcheng

Applications in research facilities - FLASH II Hall (16:15-17:45)

- Conveners: Larsen, Raymond

time	title	presenter
16:15	FLASH2/XFEL machine protection system, experience	JAEGER, Juergen M.
16:30	DESY Board Development Stage and Licensing	FENNER, Michael
16:45	Precision regulation for SRF cavities using MTCA.4	RUTKOWSKI, Igor
17:00	MicroTCA.4-based BPM and orbit feedback systems at Sirius	BRUNO, Gustavo
17:15	MTCA.4 based LLRF system using direct sampling method	GRZEGRZÓ■KA, Maciek
17:30	Test-Stand for High-Performance FPGA Computing Module	MIELCZAREK, Aleksander