



A Short Course in IC Design

Robert Patti, CTO
rpatti@tezzaron.com

A Short Course in IC Design: Outline

The lab course provides a brief introduction to the use and application of EDA tools used to create integrated circuits.

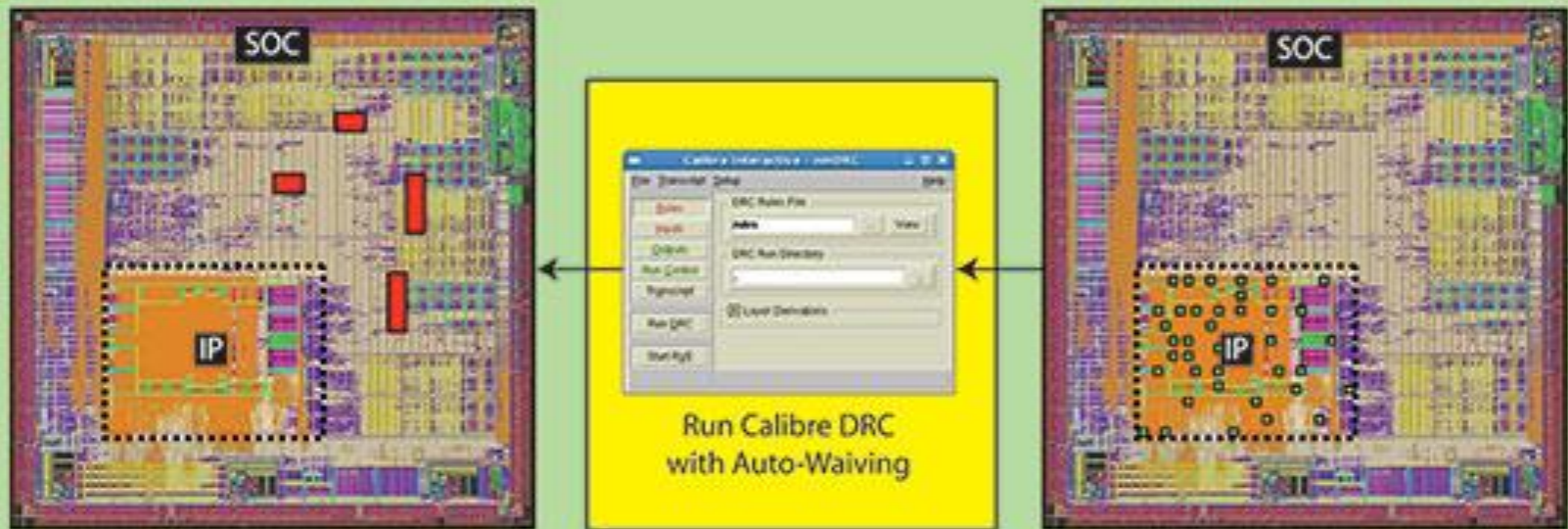
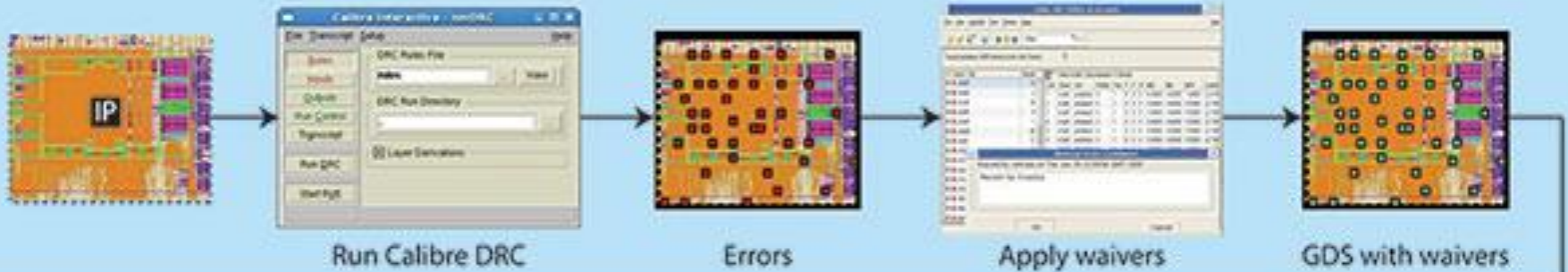
The lab is based on simple tutorials for:

- Schematic capture
- Physical layout
- Simulation
- Cross-probing
- Design rule checking (DRC)
- Layout Vs Schematic (LVS)

Mentor Calibre DRC

Mentor Calibre LVS

IP Designer



Chip Designer