



Contribution ID : 14

Type : **not specified**

**NEW FRONT-END ASIC DESIGN(s) for next  
generation of pixel based devices : How to handle  
with High granularity, high rate & high performances  
in the HEP case (HL-LHC)**

*Saturday, September 19, 2015 10:50 AM (1:15)*

**Abstract content**

**Summary**

**Presenter(s) :** Prof. GARCIA-SCIVERES, Maurice (LBL, USA)

**Session Classification :** Intelligent Silicon Trackers: Pixels and large area Si Trackers